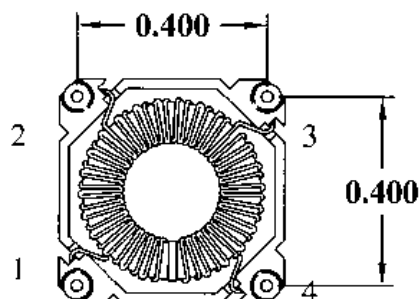
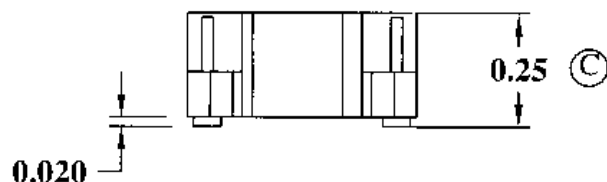
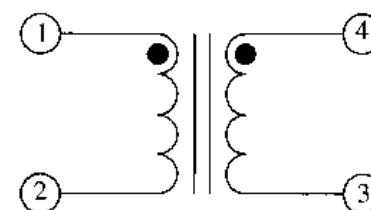
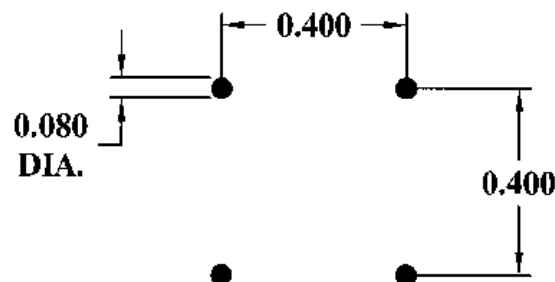
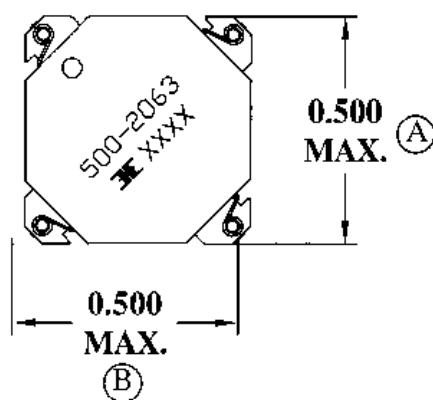


MECHANICAL

RECOMMENDED PC BOARD LAYOUT

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 64uH NOM. 100 KHz 20mV

DC RESISTANCE: 0.116 OHMS NOM.

HIGH VOLTAGE TEST: 1000 VDC WINDING TO WINDING (1-3)

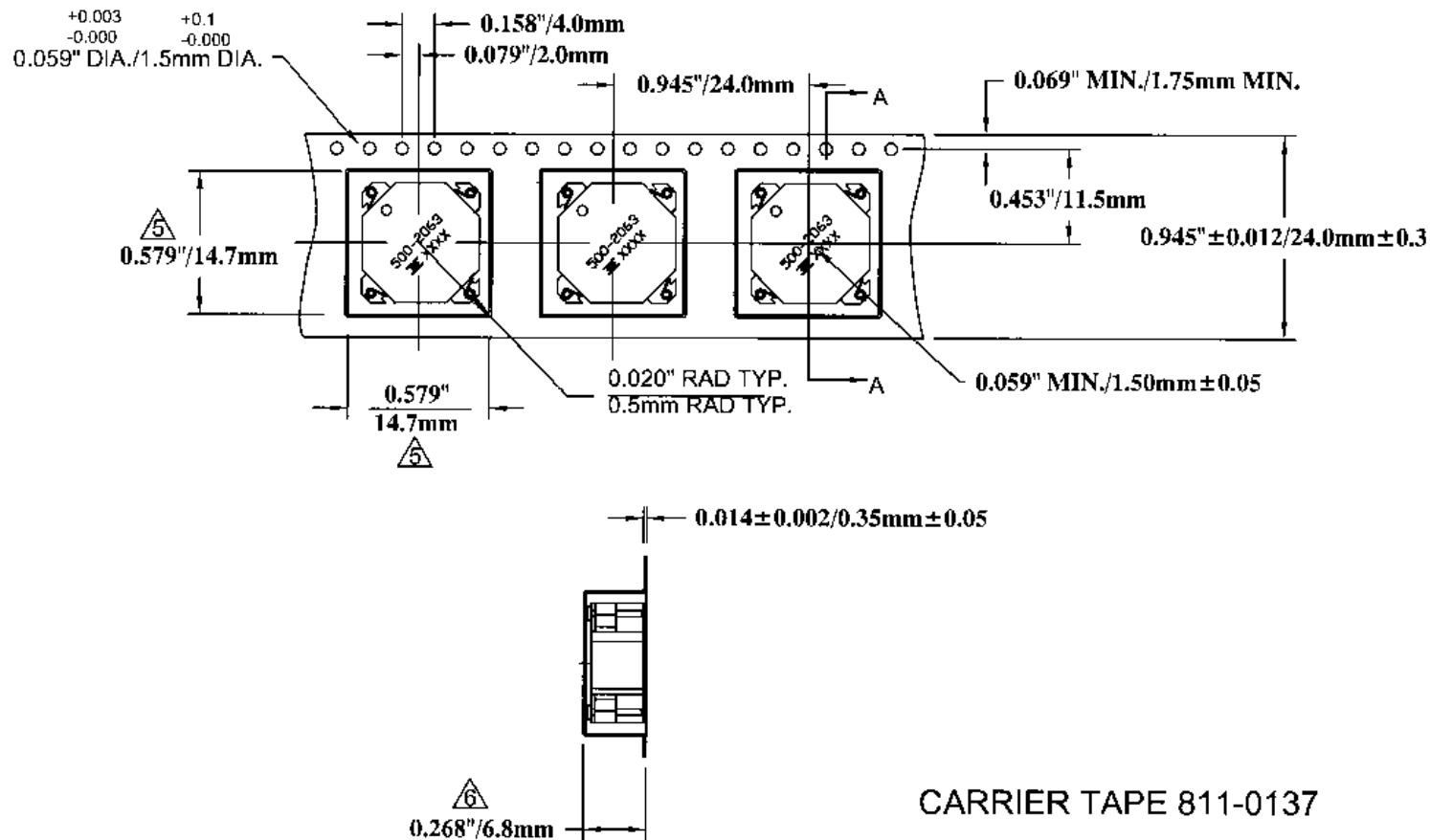
IMPEDANCE (1-2): 10 MHz 1.5K OHMS MIN.

50 MHz 2.1K OHMS MIN.

75 MHz 1.5K OHMS MIN.

100 MHz 1.2K OHMS MIN.

				DESIGN PER:	CATALOG		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN
				☐ CUSTOM			
				DRAWN:	K. LOFTUS		© 1998 BH Electronics
				ENGR:	J. DECRAMER		
K	5-11-06	SHOW NEW CASE	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005			TITLE:
J	11-11-03	INSERTION TO IMPEDANCE	JDC				COMMON MODE DUAL INDUCTOR
II	11-28-00	UPDATE TO CAD	JDC				
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2			500-2063



CARRIER TAPE 811-0137

SECTION A-A

				DESIGN PER:			
				CATALOG			
				☐ CUSTOM	NON-PROPRIETARY	☒ X	
				DRAWN:	K. LOFTUS		
				ENGR:	J. DECRAMER		
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX $\pm$.01 .XXX $\pm$.005			
K	5-11-06	SHOW NEW CASE		PAGE 2 OF 2			
J	11-11-03	INSERTION TO IMPEDANCE	JDC				
H	11-28-00	UPDATE TO CAD	JDC				
ISSUE	DATE	REVISION	BY				



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TITLE: COMMON MODE
DUAL INDUCTOR

PART NO. 500-2063