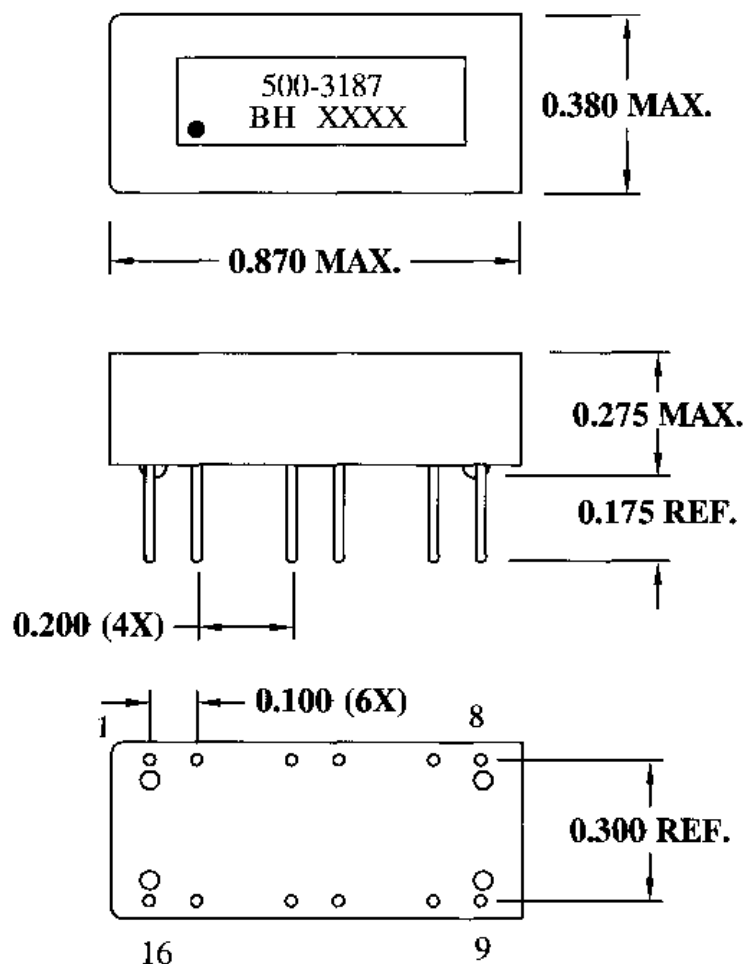
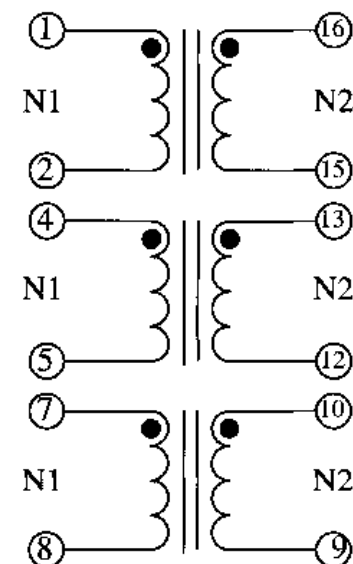


# MECHANICAL



# SCHEMATIC



## ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 80-120 $\mu$ H @ 0.020 VRMS, 100 KHz (1-2) (4-5) (7-8)

DC RESISTANCE: 0.30 OHMS MAX. (1-2) (4-5) (7-8) (16-15) (13-12) (10-9)

HIGH VOLTAGE TEST: 500 VRMS / 1 SEC.

PRIMARY LEAKAGE INDUCTANCE: 0.2 $\mu$ H MAX. (1-2) (4-5) (7-8) 120 KHz 50mV

INTERWINDING CAPACITANCE: 10pF MAX. (1-16) (4-13) (7-10) 120 KHz 50mV

PRIMARY ET CONSTANT: 2.1 Vu SEC. MIN.

PULSE INDUCTANCE: 80 $\mu$ H MIN.

TEMPERATURE CLASS: 200°C

|             |          |                     |     |                                                                                                                    |                   |
|-------------|----------|---------------------|-----|--------------------------------------------------------------------------------------------------------------------|-------------------|
| DESIGN PER: |          |                     |     | 200C ETHERNET TRANSFORMER                                                                                          |                   |
|             |          |                     |     | CUSTOM                                                                                                             | NON-PROPRIETARY X |
| DRAWN:      |          |                     |     | K. LOFTUS                                                                                                          |                   |
| ENGR:       |          |                     |     | J. DECRAMER                                                                                                        |                   |
|             |          |                     |     | UNLESS OTHERWISE SPECIFIED:<br>DIMENSIONS IN INCHES, TOLERANCES<br>NON-CUMULATIVE<br>.XX $\pm$ .01 .XXX $\pm$ .005 |                   |
|             |          |                     |     | PAGE 1 OF 1                                                                                                        |                   |
| C           | 11-11-11 | REVISED TITLE BLOCK | JDC | TITLE:<br><b>TRANSFORMER</b>                                                                                       |                   |
| B           | 10-18-11 | Q12915 NOW 500-3187 |     |                                                                                                                    |                   |
| A           | 9-30-11  | CUSTOMER PRINT      |     |                                                                                                                    |                   |
| ISSUE       | DATE     | REVISION            | BY  | PART NO.<br><b>500-3187</b>                                                                                        |                   |



BH ELECTRONICS, INC.  
BURNSVILLE & MARSHALL, MN

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