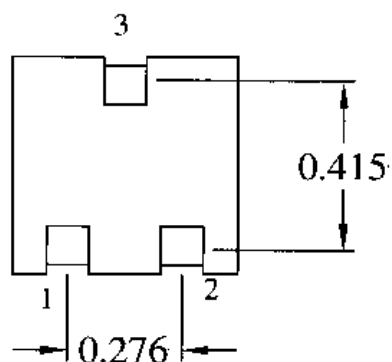
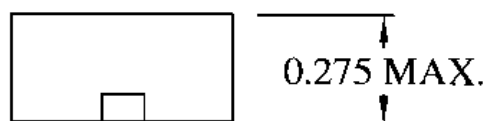
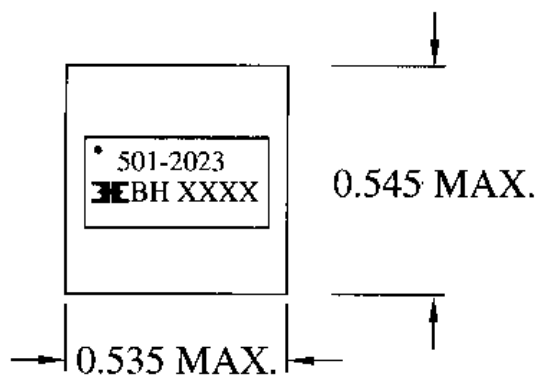
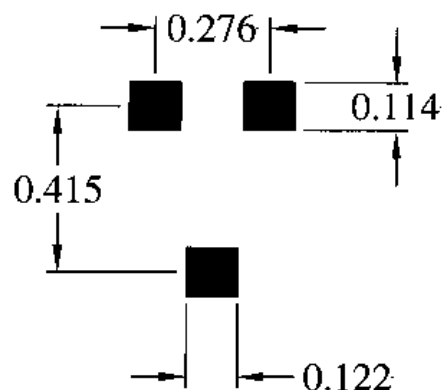


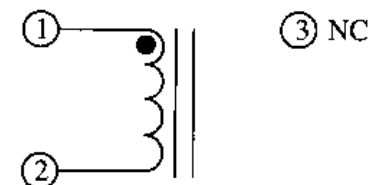
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

OPEN CIRCUIT INDUCTANCE: $4.0\mu\text{H} \pm 20\%$

DC RESISTANCE: 0.0080 OHMS MAX.

MAX. I PEAK: 10.3A (NOTE 2)

NOTE:

1. INDUCTANCE TESTED AT 100 KHz 500mV, 0 ADC
2. AVERAGE CURRENT FOR A 40°C TEMP. RISE ABOVE 25°C AMBIENT.

				DESIGN PER:	CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
F	3-19-12	NOW INDIVIDUAL PRINTS	JDC	☐ CUSTOM	NON-PROPRIETARY ☒	
E	3-5-12	DIMENSION CHANGE	JDC	DRAWN:	K. LOFTUS	TITLE: INDUCTOR
D	12-23-08	REMOVE NOTE 2, RENUMBER	JDC	ENGR:	J. DECRAMER	
C	5-23-08	UPDATE TO CATALOG	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. 501-2023
B	3-15-05	Q11641-X NOW 501-20XX				
A	2-21-05	CUSTOMER PRINT	NGD			
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		