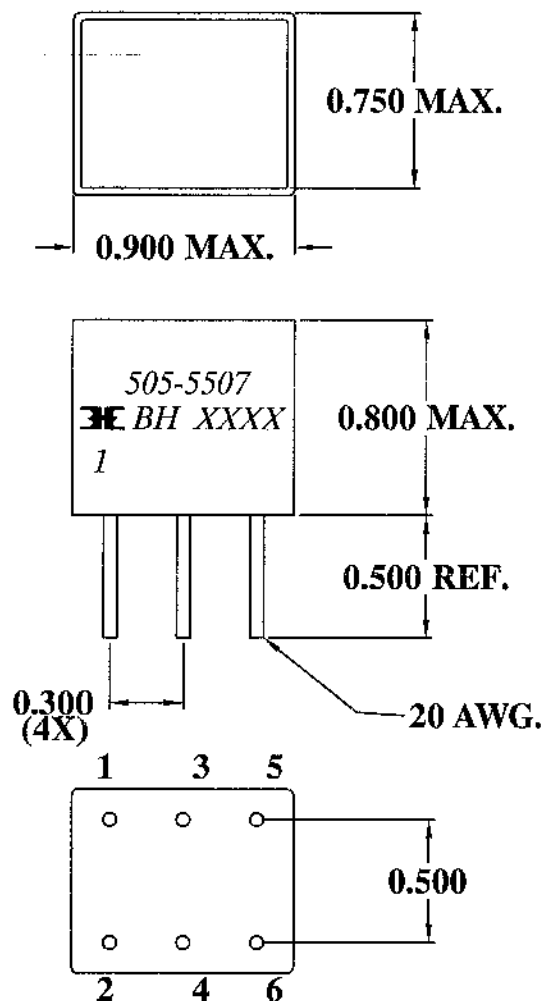
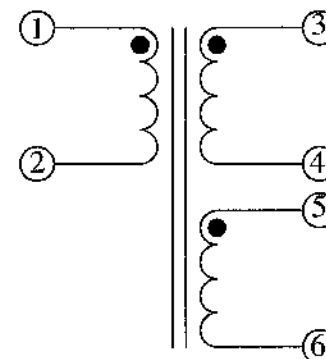


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1:1

OPEN CIRCUIT INDUCTANCE: 20mH MIN. (1-2) 1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 200μH MAX.

(1-2 SH 3-4, 5-6)

INTERWINDING CAPACTANCE: 50pF MAX.

(1, 2 - 3, 4) (1, 2 - 5, 6)

HIGH VOLTAGE TEST: 2500 VRMS / 1 SEC.

ET CONSTANT: 360 V μSEC. MIN.

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				☐ CUSTOM NON-PROPRIETARY ☒	
				DRAWN: K. LOFTUS	
C	11-7-05	ECR #6633 UPDATE TO CADD	<i>Bm</i>	ENGR: B. MARKS	TITLE: SCR TRIGGER TRANSFORMER
B	7-29-96	ECR #2830 CHG. LOC TO	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	
		1 KHz 100mV			PART NO. 505-5507
A	7-10-96	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	