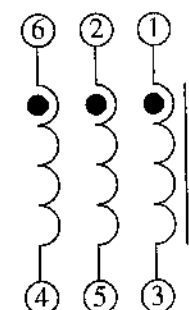
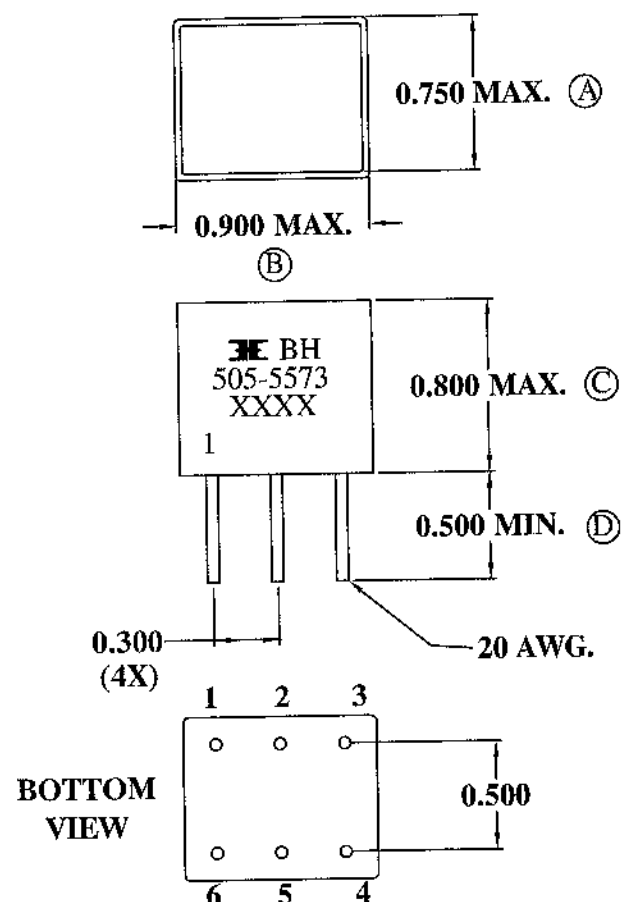


MECHANICAL

SCHEMATIC



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1:1

OPEN CIRCUIT INDUCTANCE: 1.0mH MIN. (1-3)

120 KHz 50mV

PRIMARY LEAKAGE INDUCTANCE: 20μH MAX. (1-3) (SH 2-5, 4-6)

INTERWINDING CAPACITANCE: 30pF MAX. (1-2) (1-6)

DC RESISTANCE: 2.0 OHMS MAX. (EACH WINDING)

HIGH VOLTAGE TEST: 2500 VRMS

ET CONSTANT: 90 V μ SEC. MIN.

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics	
				☐ CUSTOM			NON-PROPRIETARY ☒
				DRAWN: K. LOFTUS			
				ENGR: J. DECRAMER			
C	1-23-06	UPDATE ELECTRICALS	<i>JM</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:	
B	7-9-04	ECR #6151 CORRECT PINS	BM			PART NO.	
A	6-8-04	CUSTOMER PRINT	JDC				
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		505-5573	