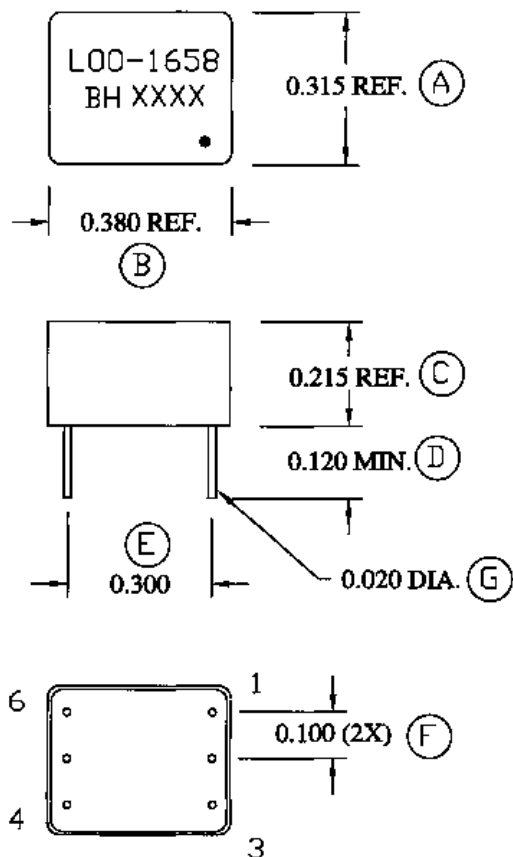
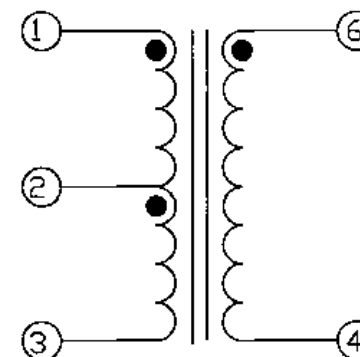


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1CT

OPEN CIRCUIT INDUCTANCE: 500μH MIN. (1-3) 100 KHz 100mV

DC RESISTANCE: 2 OHMS MAX. (1-3)

ET CONSTANT: 30 Vμ SEC. MIN. (BIPOLAR)

HIGH VOLTAGE TEST: 3750 VRMS / 1 SEC.

(RoHS COMPLIANT)

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				☐ CUSTOM ☐ NON-PROPRIETARY	
				DRAWN: K. LOFTUS	
				ENGR: T. GOODRICH	TITLE:
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE XX±.01 XXX±.005	PART NO. L00-1658
A	3-7-12	CUSTOMER PRINT			
ISSUE	DATE	REVISION	BY		
				PAGE 1 OF 1	