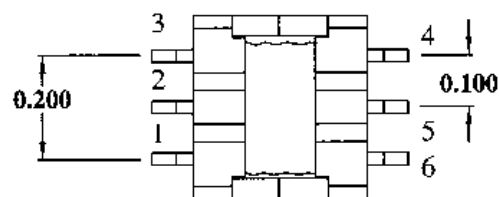
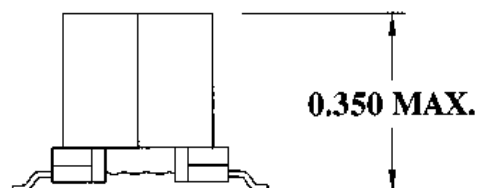
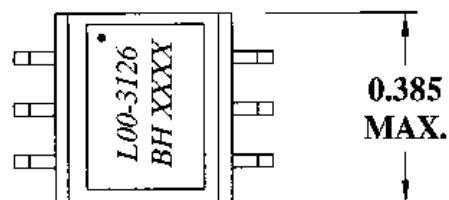
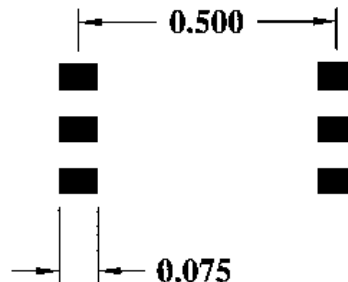


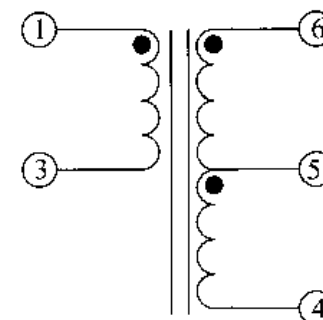
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS: 1:6

OPEN CIRCUIT INDUCTANCE: 72 - 88 μ H (1-3) 100 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 0.38 μ H NOM. (1-3 SH 4, 5, 6) 120 KHz 50mV

INTERWINDING CAPACITANCE: 40pF NOM. (1-6) 120 KHz 50mV

DC RESISTANCE: 0.155 OHMS NOM. (1-3) 9.43 OHMS NOM. (6-4)

SRF: 681 KHz NOM. (4-6)

HIGH VOLTAGE TEST: 1600 VAC (1-6)

DESIGN PER:

BH CATALOG

☐ CUSTOM

☐ NON-PROPRIETARY

☒ X

DRAWN: K. LOFTUS

ENGR: J. DECRAMER

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE
.XX $\pm$.01 .XXX $\pm$.005

PAGE 1 OF 1



BH ELECTRONICS, INC.
BURNSVILLE & MARSHALL, MN

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TITLE:

TRANSFORMER

PART NO.

L00-3126

ISSUE	DATE	REVISION	BY
D	8-3-10	REVISE MECHANICALS	JDC
C	5-11-10	URNS CHANGE	JDC
B	4-20-10	Q12713 NOW L00-3126	JDC
A	4-14-10	CUSTOMER PRINT	JDC