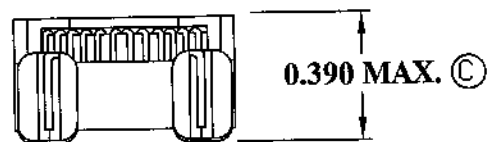
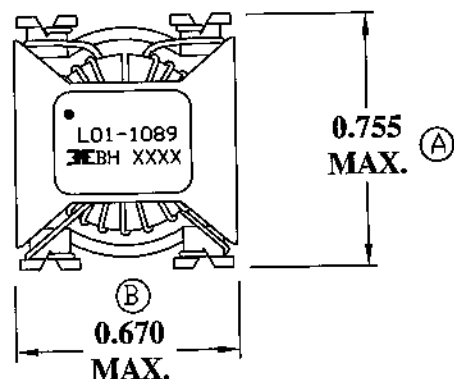
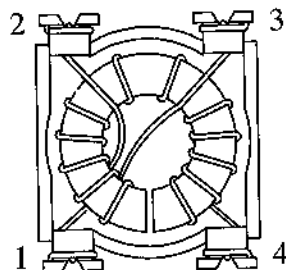


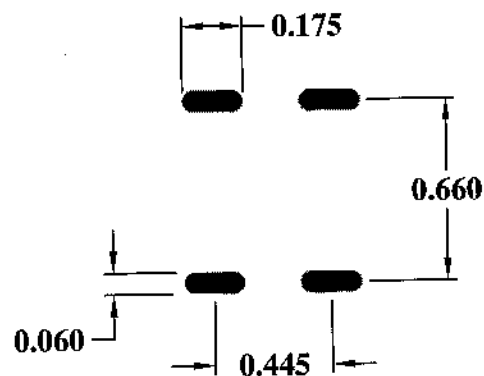
MECHANICAL



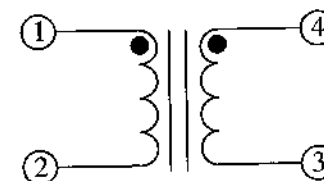
COPLANAR 0.004



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 4.23μH±10% @ 0 ADC (1-2)
10 KHz 100mV

3.1μH TYP. @ 9.5 ADC (1-2) 10 KHz 100mV

DC RESISTANCE: 0.0089 OHMS NOM. (1-2) (3-4)

HIGH VOLTAGE TEST: 500 VRMS (1-3)

RoHS Compliant

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
			☐ CUSTOM	NON-PROPRIETARY ☒	
			DRAWN: K. LOFTUS		
			ENGR: J. DECRAMER		
B	11-7-05	ECR #6634 INC. WIDTH SPEC.	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	TITLE:
A	9-20-05	CUSTOMER PRINT	JDC		PART NO.
					L01-1089
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	