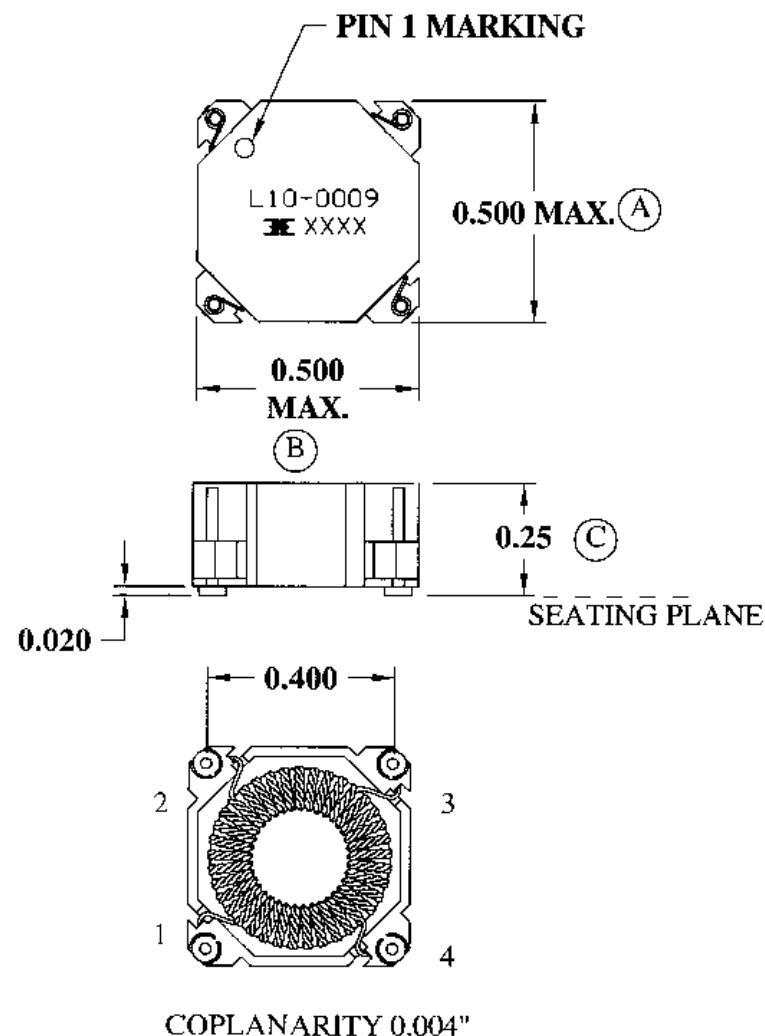
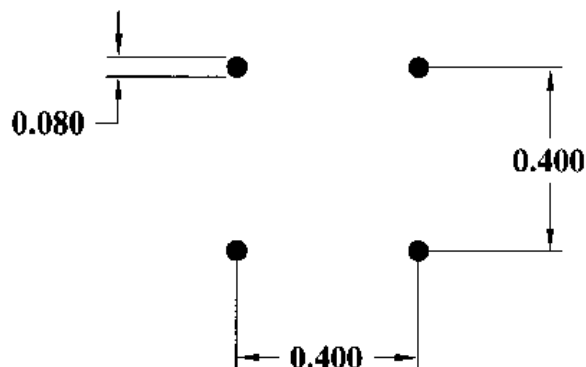


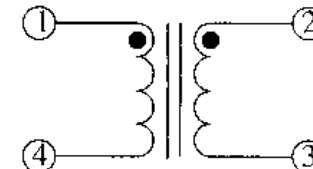
MECHANICAL



RECOMMENDED PCB LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 9mH MIN. @ 0 ADC (1-4) 10 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 0.5μH NOM. (1-4 SH 2, 3)
120 KHz 50mV

INTERWINDING CAPACITANCE: 55pF NOM. (1-2) 120 KHz 50mV

DC RESISTANCE: 1.125 OHMS NOM. (1-4) (2-3)

IR: 9K MEGOHMS MIN. @ 200 VDC (1-2)

RoHS Compliant

				DESIGN PER:	 [®] BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				☐ CUSTOM NON-PROPRIETARY ☒	
				DRAWN: K. LOFTUS	TITLE: CHOKE
				ENGR: B. GASS	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	
				PAGE 1 OF 2	
C	1-28-08	SHOW BRANDED	BG		PART NO. L10-0009
B	9-13-05	SHOW NEW CASE	JDC		
A	3-16-05	CUSTOMER PRINT			
ISSUE	DATE	REVISION	BY		

