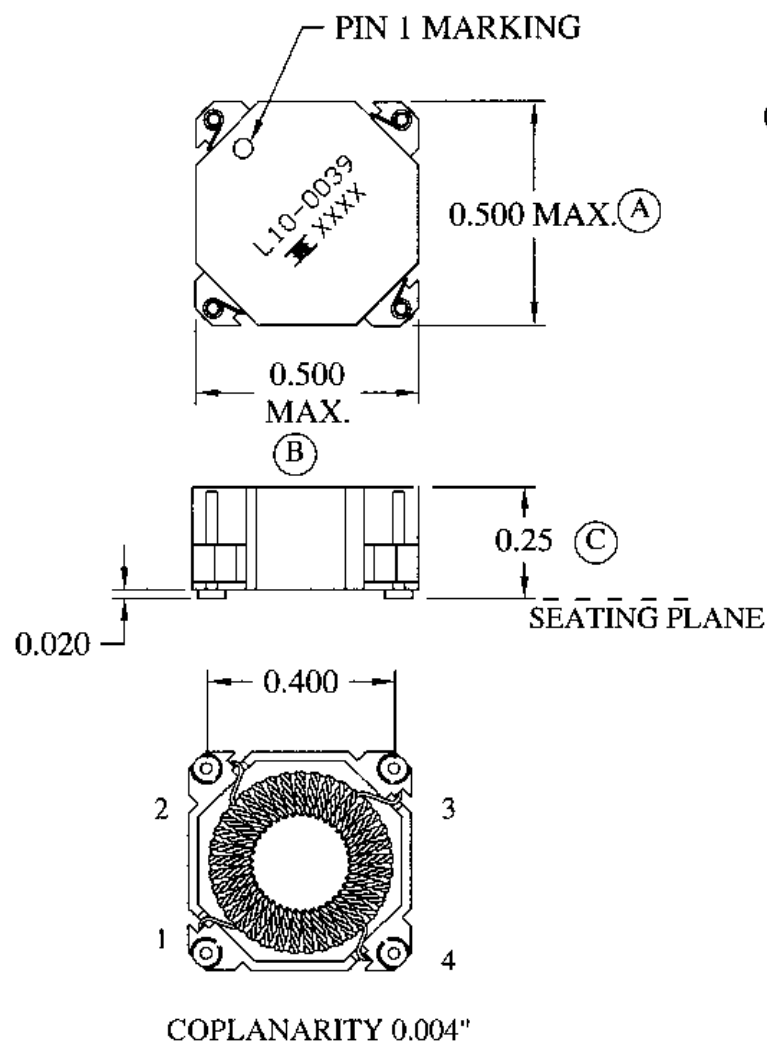
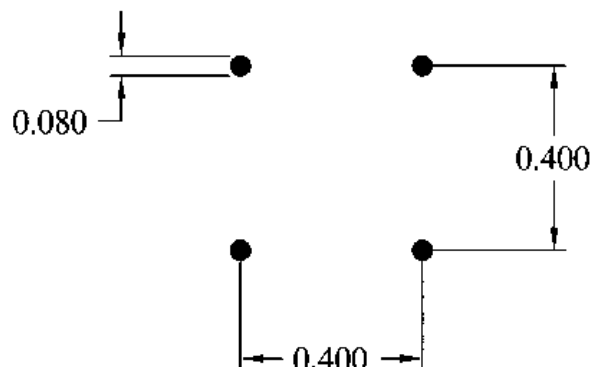


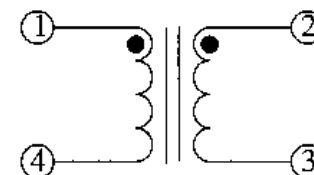
MECHANICAL



RECOMMENDED PCB LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 36mH MIN. @ 0 ADC (1-4) 10 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 1.0μH NOM. (1-4 SH 2, 3)

120 KHz 50mV

INTERWINDING CAPACITANCE: 120pF NOM. (1-2) 120 KHz 50mV

DC RESISTANCE: 2.500 OHMS NOM. (1-4) (2-3)

IR: 5K MEGOHMS MIN. @ 200 VDC (1-2)

RoHS Compliant

DESIGN PER:

BH CATALOG

☐ CUSTOM

NON-PROPRIETARY ☒

DRAWN: K. LOFTUS

ENGR: B. GASS

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE
.XX±.01 .XXX±.005

PAGE 1 OF 2



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BURNSVILLE & MARSHALL, MN

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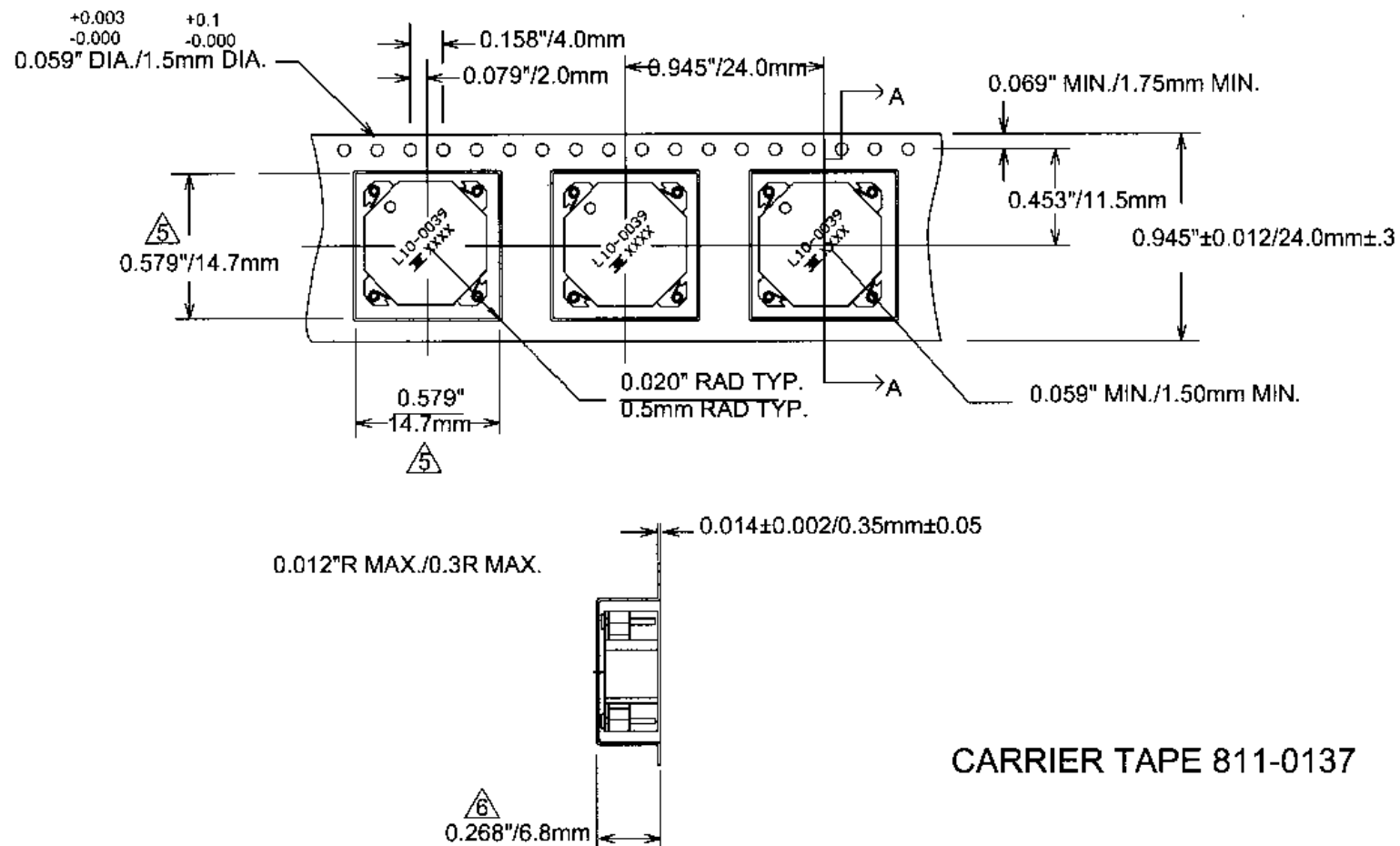
TITLE:

CHOKE

PART NO.

L10-0039

A	1-14-09	CUSTOMER PRINT (Q12524)	B.G.
ISSUE	DATE	REVISION	BY



CARRIER TAPE 811-0137

SECTION A-A

				DESIGN PER:	BH CATALOG	 [®] BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				☐ CUSTOM		
				DRAWN: K. LOFTUS		
				ENGR: J. DECRAMER		
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE: CHOKE
A	3-16-05	CUSTOMER PRINT (Q12524)				PART NO. L10-0039
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2		