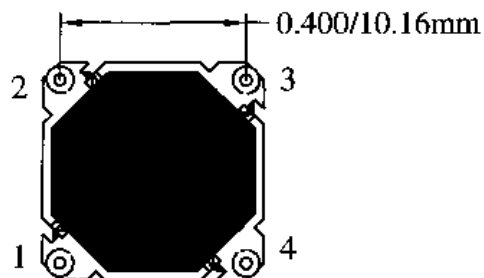
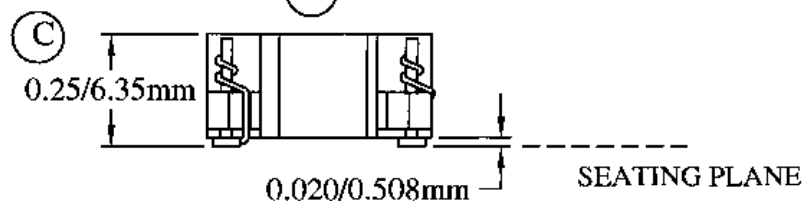
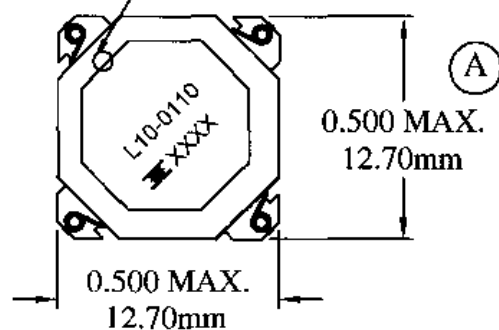


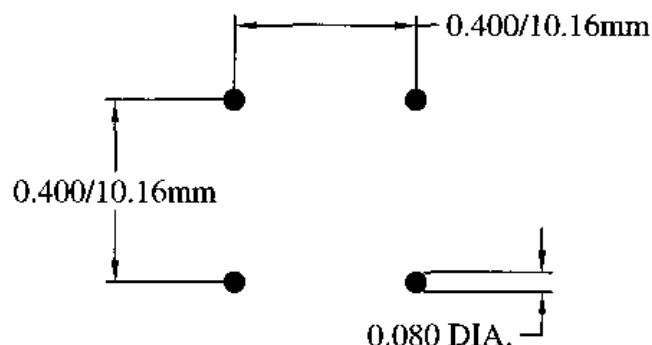
MECHANICAL

PIN 1 MARKING

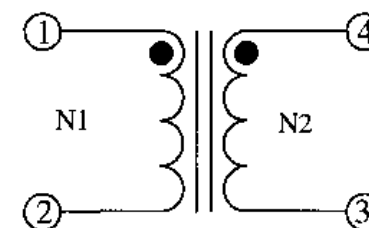


COPLANARITY 0.004"

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 24.8 μ H $\pm$ 20% (1-2) (4-3) 100 KHz 100mV

INDUCTANCE WITH BIAS: 19.8 μ H TYP. @ 1.22ADC

PRIMARY LEAKAGE INDUCTANCE: 345nH NOM. (1-2 SH 4, 3) 120 KHz 50mV

INTERWINDING CAPACITANCE: 8pF NOM. (1-4) 120 KHz 50mV

DC RESISTANCE: 1.00 OHMS NOM. (1-2) (4-3)

HIGH VOLTAGE TEST: 4000 VAC / 1 MIN. (1-4)

5000 VAC / 1 MIN. (1-4) EXTERNAL ENVIRONMENTAL
CONDITIONS CAN AFFECT RESULTS.

(RoHS COMPLIANT)

DESIGN PER:

BH CATALOG

☐ CUSTOM

☒ NON-PROPRIETARY

DRAWN:

K. LOPTUS

ENGR:

J. DECRAMER

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE
.XX $\pm$.01 .XXX $\pm$.005

PAGE 1 OF 2



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BURNSVILLE & MARSHALL, MN

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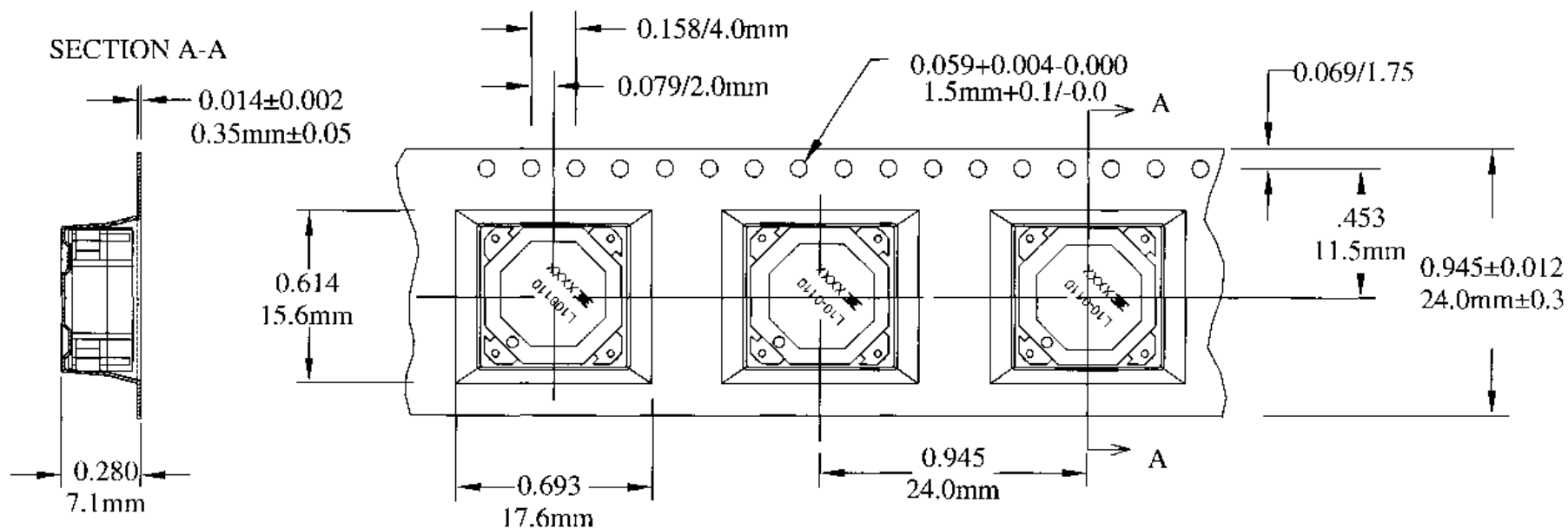
TITLE:

FLYBACK TRANSFORMER

PART NO.

L10-0110

B	6-23-11	Q12761-1 NOW L10-0110	JDC
A	6-15-11	CUSTOMER PRINT	JDC
ISSUE	DATE	REVISION	BY



CARRIER TAPE 811-0137

				DESIGN PER:	[®] BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				☐ CUSTOM NON-PROPRIETARY ☒	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	
B	6-24-11	Q12761-1 NOW L10-0110	 A	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 2 OF 2	TITLE:
A	6-15-11	CUSTOMER PRINT			FLYBACK TRANSFORMER
ISSUE	DATE	REVISION	BY		PART NO.
					L10-0110