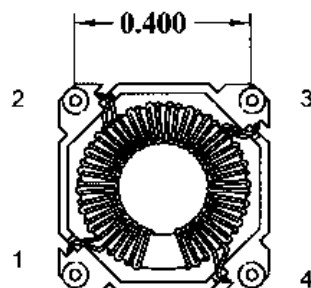
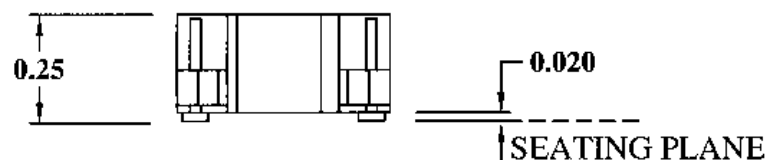
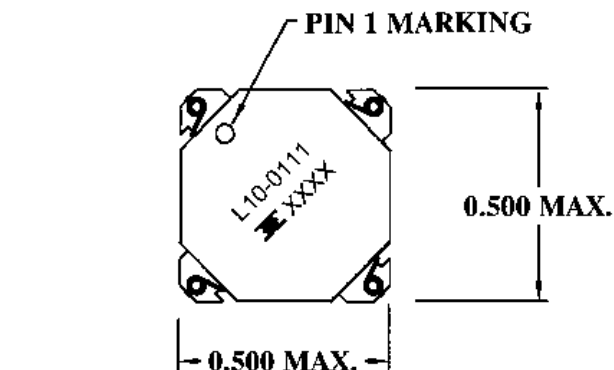


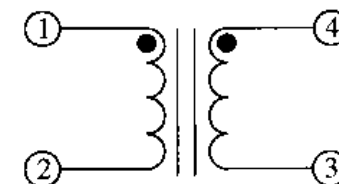
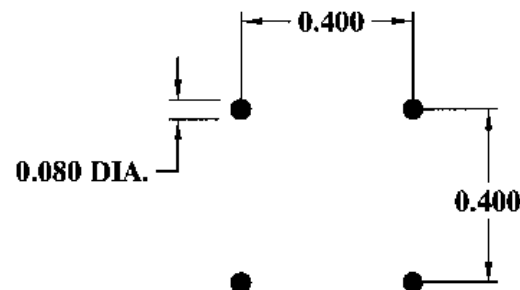
MECHANICAL

RECOMMENDED PC BOARD LAYOUT

SCHEMATIC



COPLANARITY 0.004" [0.1016]



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:2

OPEN CIRCUIT INDUCTANCE: 150.0μH ±20% @ 0 ADC (1-2)

10 KHz 100mV

DC BIAS: 141.0μH TYP. @ 1.1 ADC (1-2)

10 KHz 100mV

DC RESISTANCE: 0.64 OHMS NOM. (1-2)

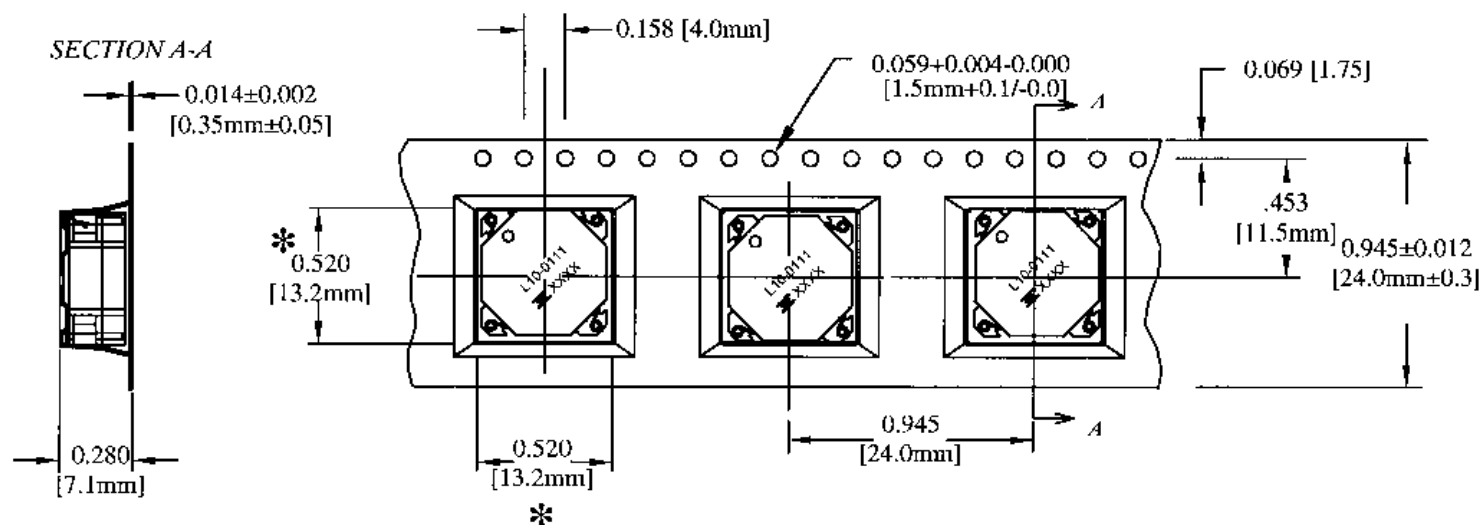
2.94 OHMS NOM. (4-3)

HIGH VOLTAGE TEST: 1000 VAC (1-4)

PRIMARY LEAKAGE INDUCTANCE: 1.29μH NOM. (1-2) (SH 4, 3) 120 KHz 50mV

RoHS Compliant

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				☒ CUSTOM ☐ NON-PROPRIETARY	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	TITLE: QTR PAK™ (Switching Regulator)
B	8-9-11	Q12786-4 NOW L10-0111	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO. L10-0111
A	4-26-11	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	



CARRIER TAPE 811-0137

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				DRAWN:	K. LOFTUS		
				ENGR:	J. DECRAMER		
B	8-9-11	Q12786-4 NOW L10-0111		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:	QTR PAK™ (Switching Regulator)
A	4-26-11	CUSTOMER PRINT	JDC			PART NO.	L10-0111
ISSUE	DATE	REVISION	BY			PAGE 2 OF 2	