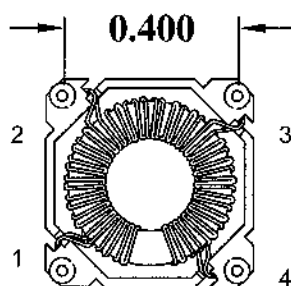
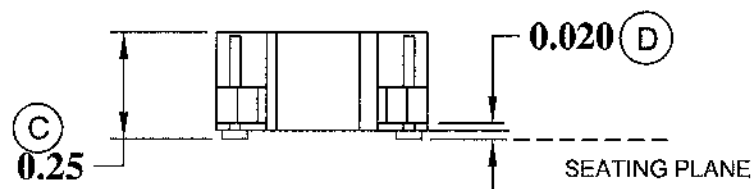
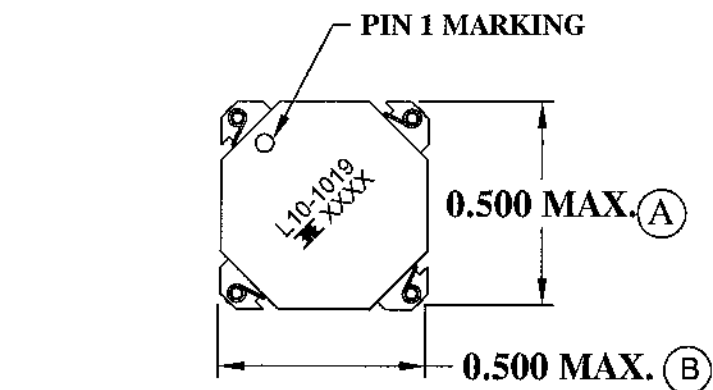
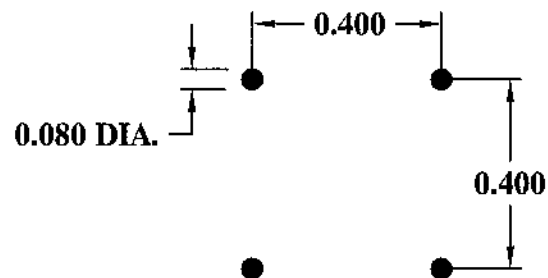


MECHANICAL

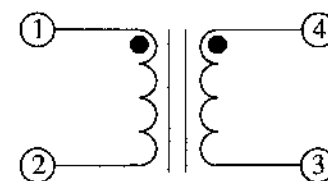


COPLANARITY 0.004"

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 14.60-21.90 μ H @ 0 ADC
(1, 4 - 2, 3) 10 KHz 100mV

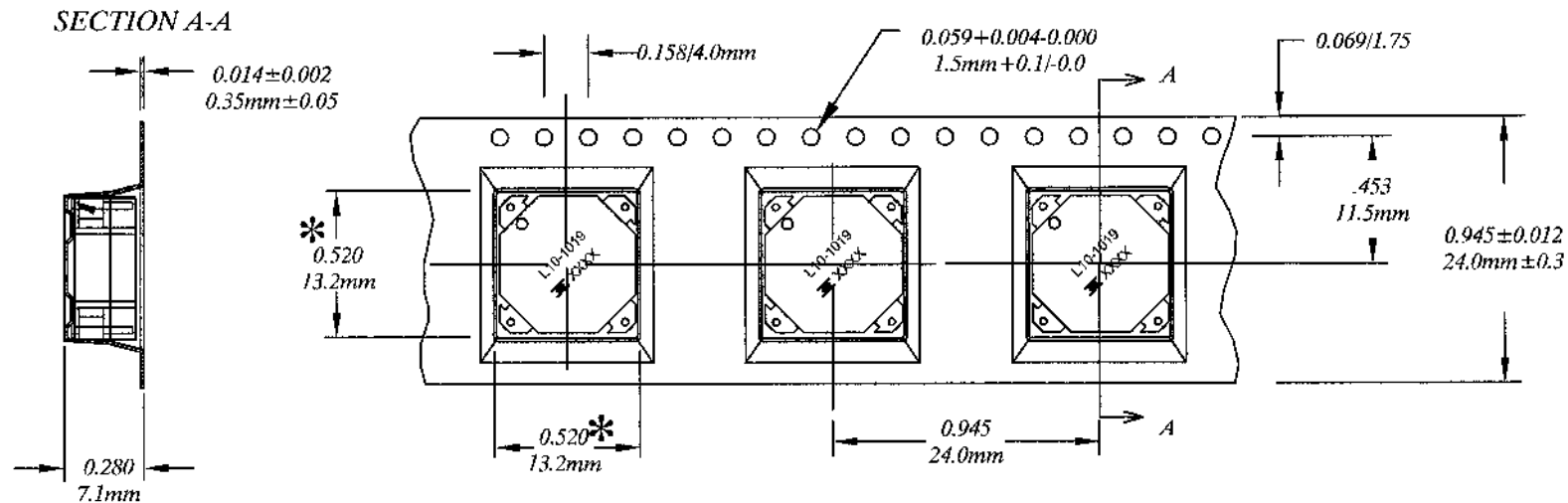
DC BIAS: 14.60 μ H TYP. @ 3.24 ADC
(1, 4 - 2, 3) 10 KHz 100mV

DC RESISTANCE: 0.0748 OHMS NOM. (1-2)
0.0748 OHMS NOM. (4-3)

IR: 100 VDC (1-4) 1K MEGOHMS MIN.

RoHS Compliant

				DESIGN PER:	 [®] BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				☐ CUSTOM NON-PROPRIETARY ☒	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	TITLE:
B	7-10-06	UPDATE TO NEW FORMAT	<i>low</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	QTR PAK™ (Switching Regulator)
A	2-18-98	CUSTOMER PRINT	JDC		PART NO.
					L10-1019
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2	



CARRIER TAPE 811-0137

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				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	
B	7-10-06	UPDATE TO NEW FORMAT		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 2 OF 2	TITLE: QTR PAK™ (Switching Regulator)
A	2-18-98	CUSTOMER PRINT	JDC		PART NO. L10-1019
ISSUE	DATE	REVISION	BY		