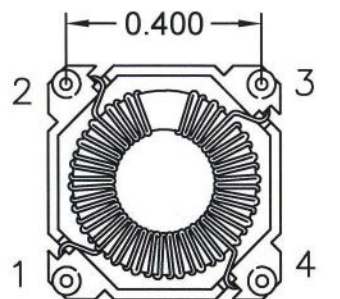
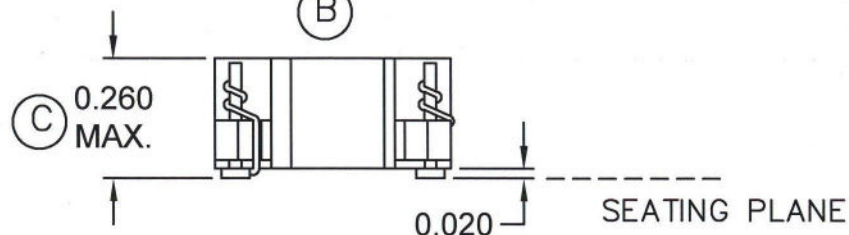
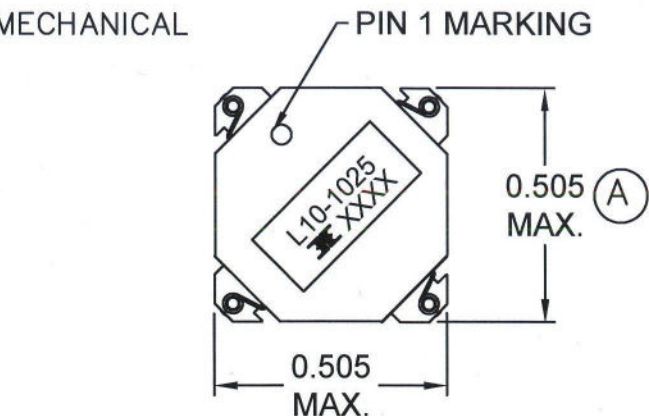
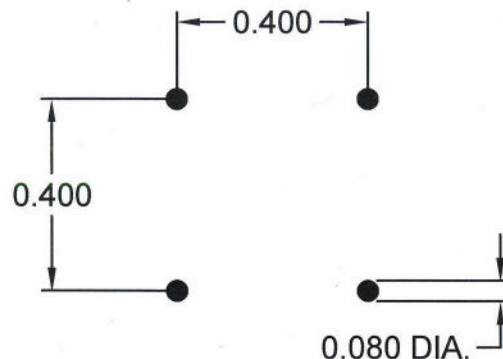


MECHANICAL

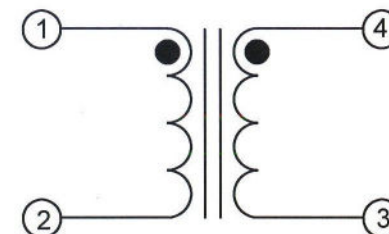


COPLANARITY 0.004"

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 34.86 μ H - 52.30 μ H @ 0 ADC (1,4-2,3)

10 KHz 100mV

DC BIAS: 34.86 μ H TYP. @ 2.09 ADC (1, 4 - 2, 3)

10 KHz 100mV

DC RESISTANCE: (1-2)(4-3) 0.19 OHMS NOM.

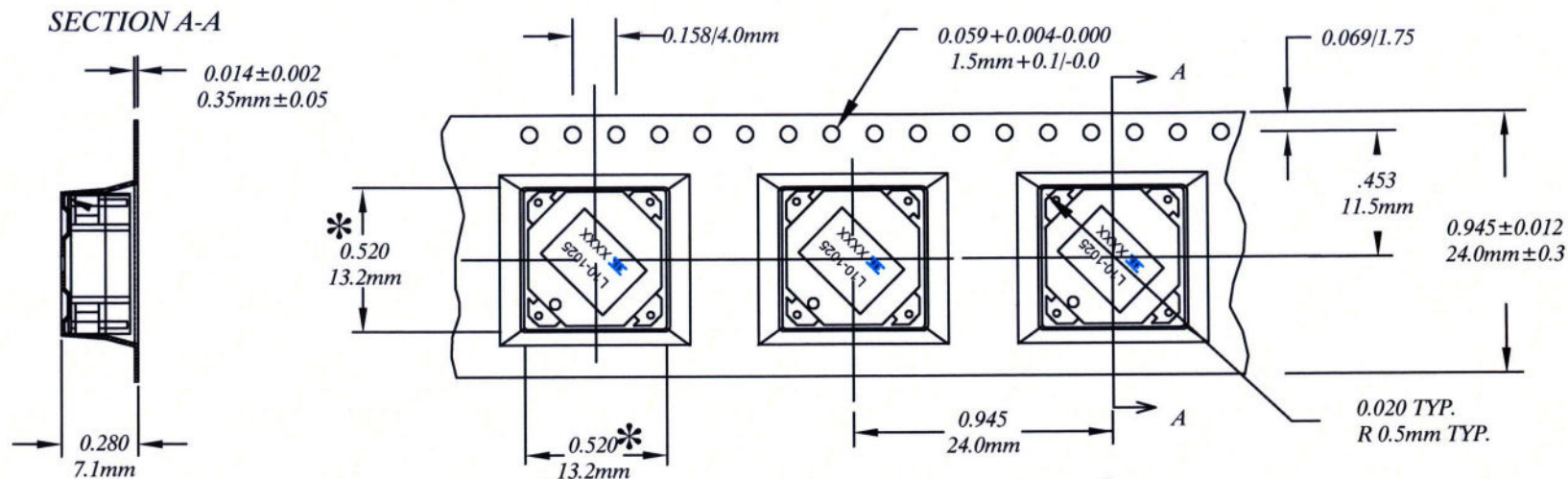
IR: 100 VDC (1-4) 1K MEGOHMS MIN.

PRIMARY LEAKAGE INDUCTANCE: 330nH NOM. (1-2 SH 3,4)

120 KHz 50 mV

RoHS Compliant

				DESIGNED PER:	 ® BH Electronics, Inc. Burnsville & Marshall, MN © 1998 BH Electronics
				BH CATALOG	
				☐ CUSTOM ☐ NON-PROPRIETARY ☒	
				DRAWN Kathy Loftus	
C	2-27-19	UPDATE ELECTRICALS	B.G.	ENGR. J. DECRAMER	TITLE: TM
B	9-13-05	ADD RoHS COMPLIANT	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005	INDUCTOR
A	5-19-05	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2	PART NO. L10-1025



CARRIER TAPE 811-0137

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				X BH CATALOG CUSTOMNON-PROPRIETARY	
				DRAWN Kathy Loftus	
				ENGR. J. DECRAMER	
C	2-27-19	UPDATE ELECTRICALS	B6	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005	TITLE: TM
B	9-13-05	ADD RoHS COMPLIANT	JDC		INDUCTOR
A	5-19-05	CUSTOMER PRINT	JDC		PART NO.
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2	L10-1025