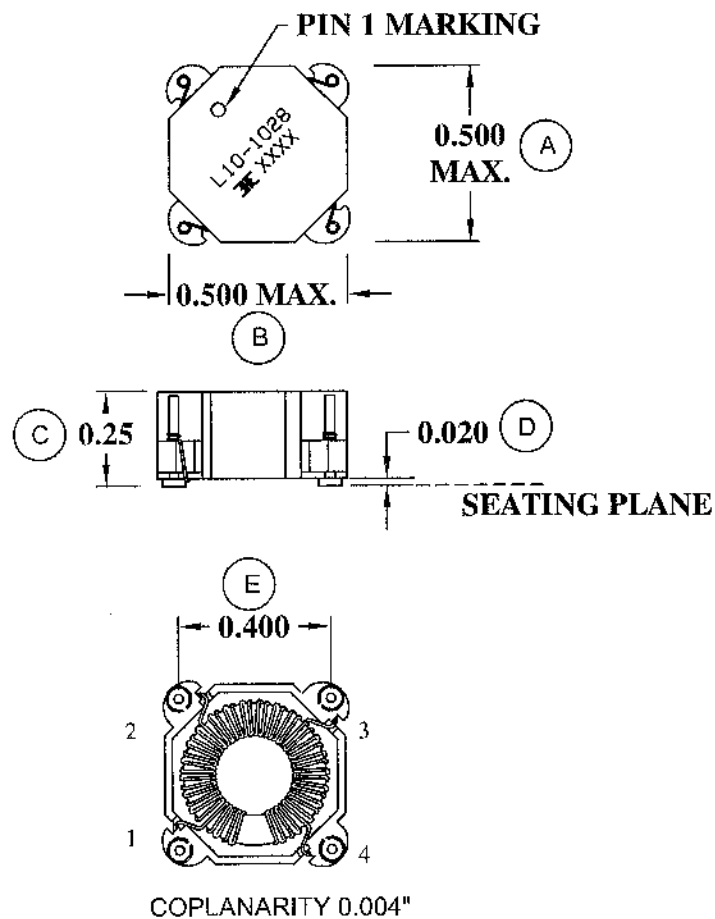
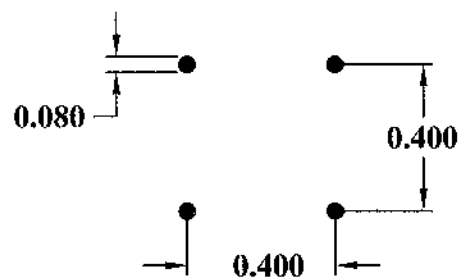


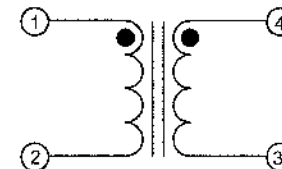
MECHANICAL



RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUTANCE: 48.26 - 72.38 μ H @ 0 ADC (1-2)
10 KHz 100mV

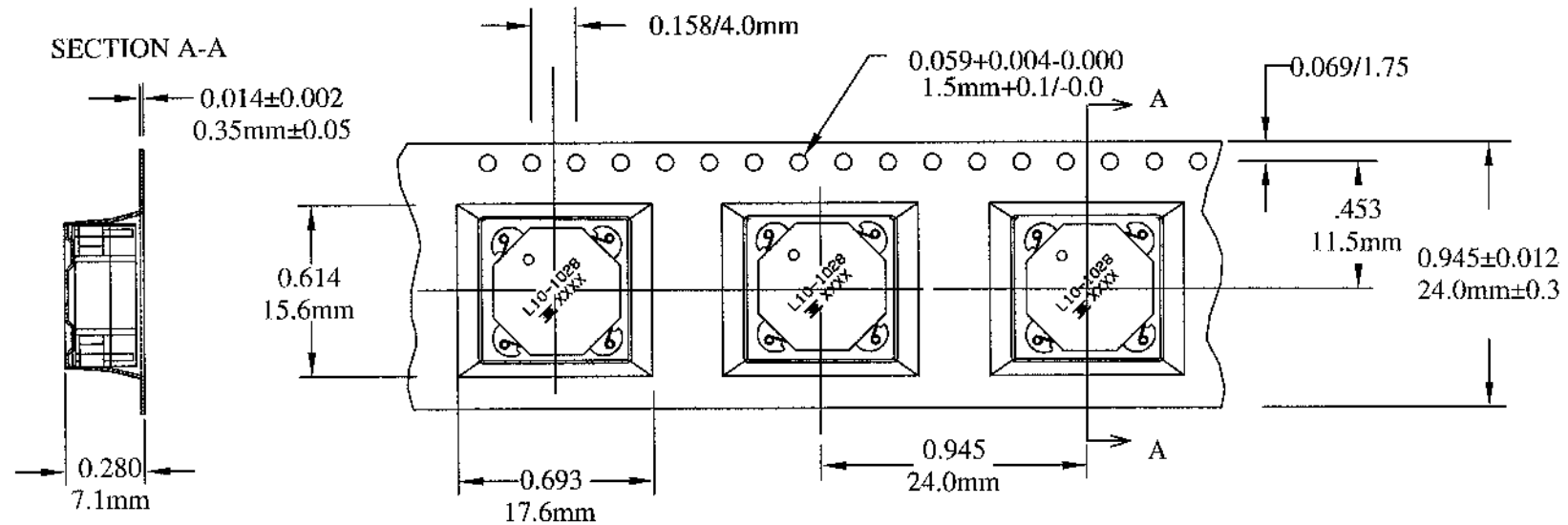
DC BIAS: 48.26 μ H TYP. @ 1.78 ADC (1-2)
10 KHz 100mV

DC RESISTANCE: (1-2) 0.2727 OHMS NOM.
(4-3) 0.2727 OHMS NOM.

IR: 100 VDC (1-4) 1K MEGOHMS MIN.

RoHS Compliant

				DESIGNED PER:	 BH Electronics, Inc. Burnsville & Marshall, MN © 1998 BH Electronics		
				BH CATALOG			
			☐	CUSTOM		NON-PROPRIETARY	☒
				DRAWN Kathy Loftus			
C	1-15-07	CHG GAP PLACEMENT		ENGR.	J. DECRAMER	TITLE: QTR PAK TM (Inductor, Switching Regulator)	
B	9-13-05	ADD RoHS COMPLIANT	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005			
A	3-16-05	CUSTOMER PRINT	JDC			PART NO. L10-1028	
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2			



CARRIER TAPE 811-0137

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				BH CATALOG		
				☐ CUSTOM	NON-PROPRIETARY ☒	
				DRAWN Kathy Loftus		
C	1-15-07	CHG GAP PLACEMENT		ENGR. J. DECRAMER		TITLE: QTR PAK [™] (Switching Regulator)
B	9-13-05	ADD RoHS COMPLIANT	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005		
A	5-19-05	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2		PART NO. L10-1028