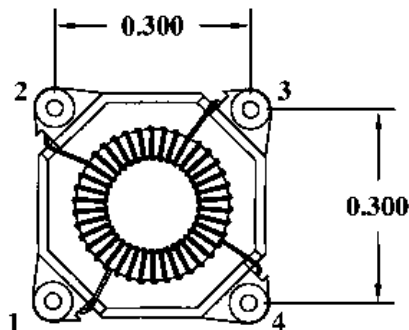
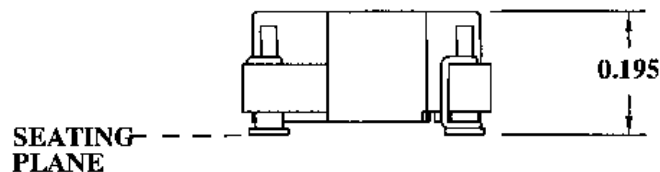
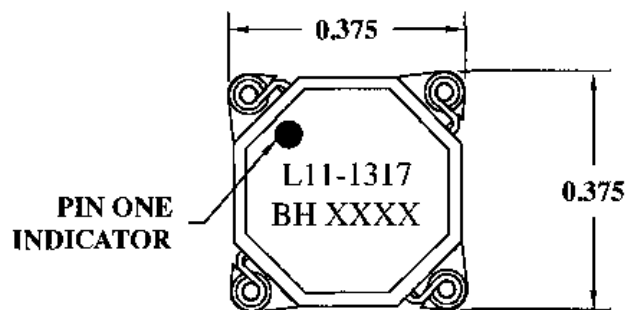
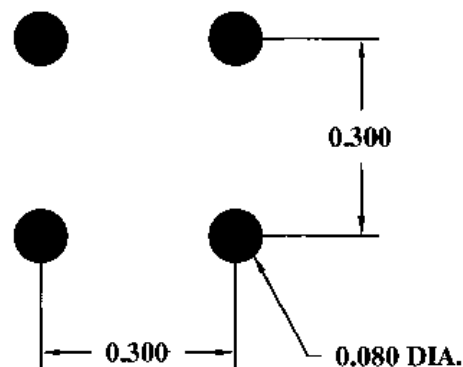


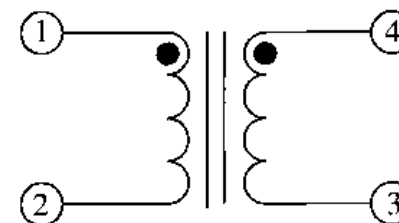
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 24.81 μ H $\pm$ 20% @ 0 ADC (1, 4 - 2, 3) 10 KHz 50mV

19.8 μ H TYP. @ 1.22 ADC (1, 4 - 2, 3) 10 KHz 100mV

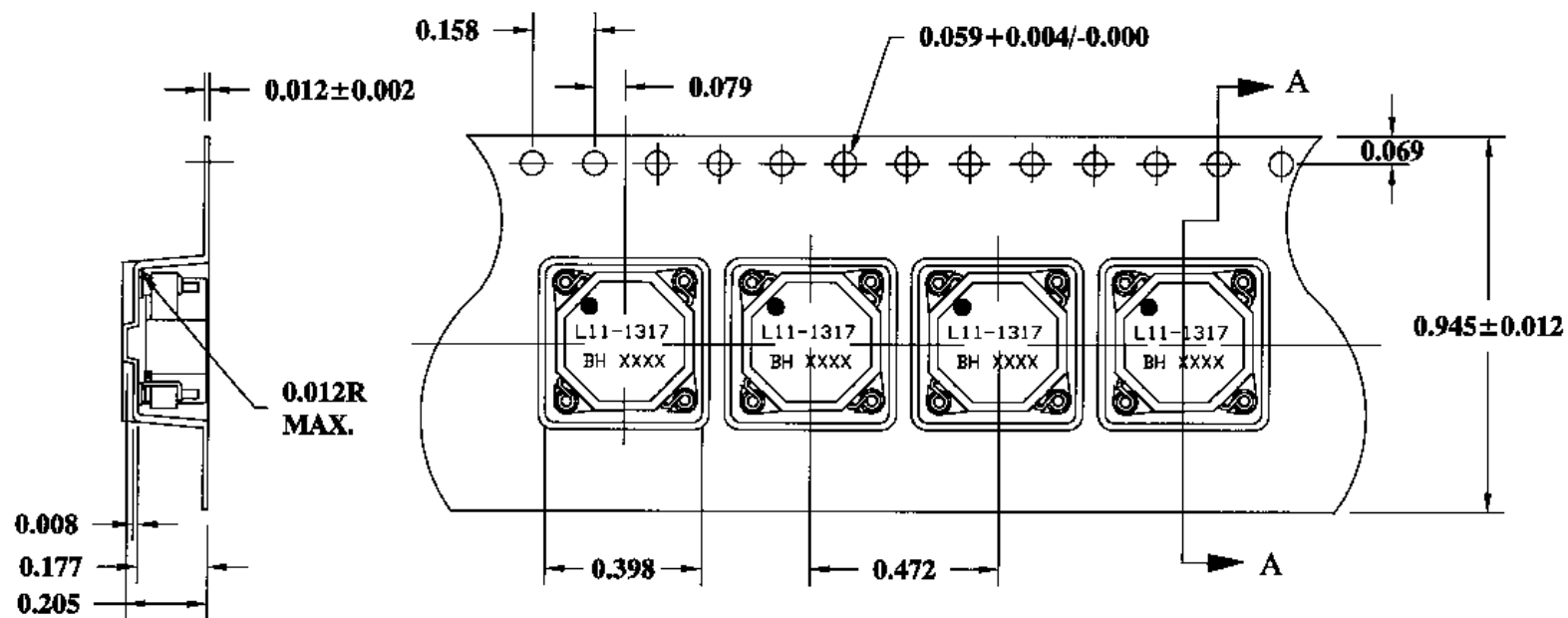
PRIMARY LEAKAGE INDUCTANCE: 0.25 μ H MAX. (1-2 SH 4, 3) 120 KHz 50mV

DC RESISTANCE: 0.40 OHMS MAX. (1-2) (3-4)

HIGH VOLTAGE TEST: 3000 VAC (1-4)

RoHS COMPLIANT

				DESIGN PER:	CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics		
				☐ CUSTOM	NON-PROPRIETARY		☒	
				DRAWN:	K. LOFTUS			
				ENGR:	J. DECRAMER			
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005			TITLE:	INDUCTOR
A	4-25-11	CUSTOMER PRINT					PART NO.	L11-1317
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2				



CARRIER TAPE 811-0187

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				DRAWN: K. LOFTUS				
				ENGR: J. DECRAMER		TITLE: INDUCTOR		
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE XX±.01 XXX±.005		PART NO. L11-1317		
A	4-25-11	CUSTOMER PRINT		PAGE 2 OF 2				
ISSUE	DATE	REVISION	BY					