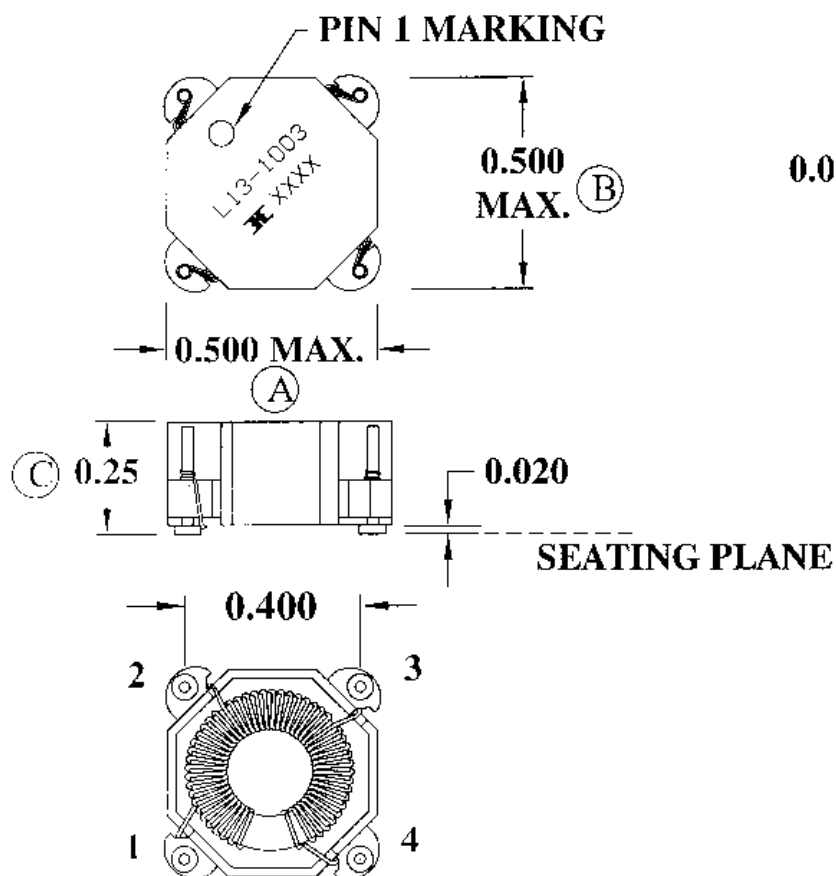
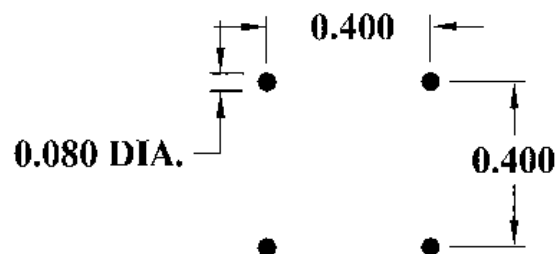


MECHANICAL

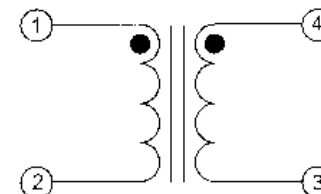


COPLANARITY 0.004"

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 3.21μH±10% @ 0 ADC

(1-2) (SH 1, 4) (SH 2, 3) 10 KHz 50mV

DC BIAS: 2.09μH TYP. @ 9.82 ADC

(1-2) (SH 1, 4) (SH 2, 3) 10 KHz 50mV

DC RESISTANCE: 0.0127 OHMS MAX. (1-2) (SH 1, 4) (2, 3)

IR: 100 VDC (1-4) 1K MEGOHMS MIN.

DESIGNED PER:

BH CATALOG

☐ CUSTOM

☒ NON-PROPRIETARY

DRAWN K. LOFTUS

ENGR. J. DECRAMER

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE.

.XX±.01 .XXX±.005

PAGE 1 OF 2



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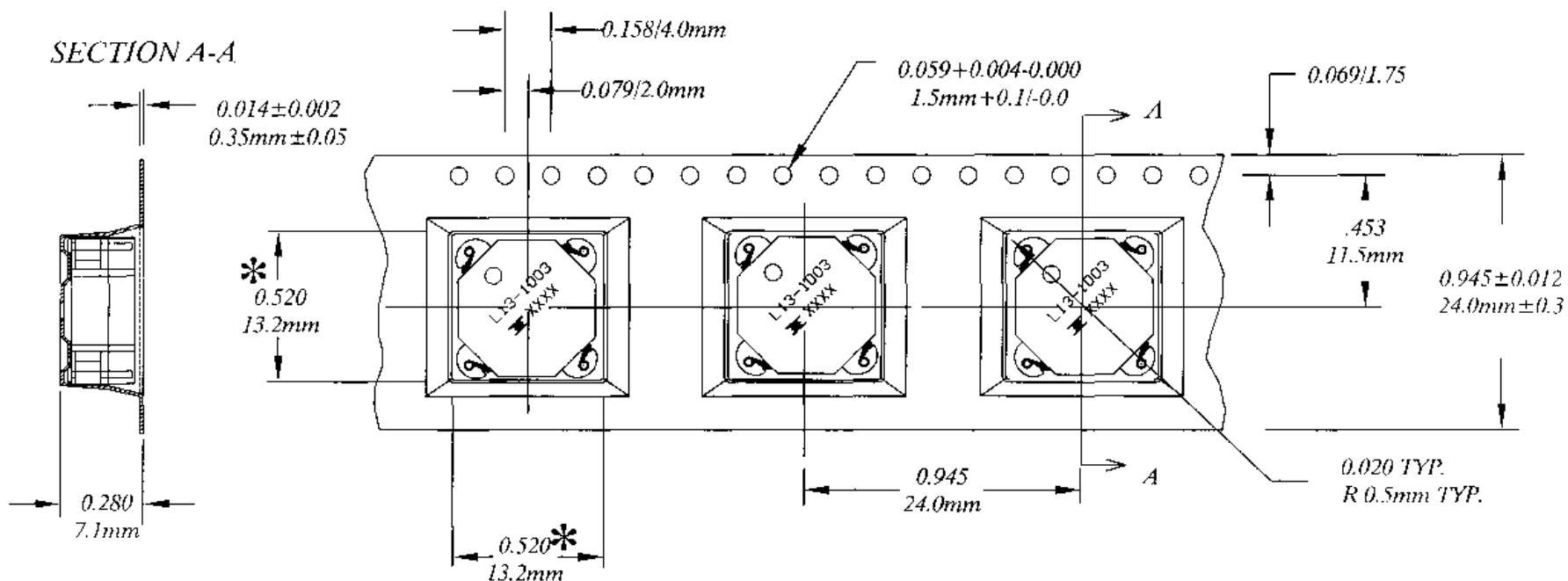
TITLE:

QTR PAK™
(Switching Regulator)

PART NO.

L13-1003

B	12-23-09	REVISE ELECTRICAL PINOUT	gde
A	4-5-07	CUSTOMER PRINT	JDC
ISSUE	DATE	REVISION	BY



CARRIER TAPE 811-0137

				DESIGNED PER:	 BH Electronics, Inc. Burnsville & Marshall, MN © 1998 BH Electronics	
				BH CATALOG		
				☐ CUSTOM ☐ NON-PROPRIETARY ☒		
				DRAWN K. LOFTUS		
				ENGR. J. DECRAMER	TITLE:	QTR PAK™ (Switching Regulator)
B	12-23-09	REVISE ELECTRICAL PINOUT		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005	PART NO.	L13-1003
A	4-5-07	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2		