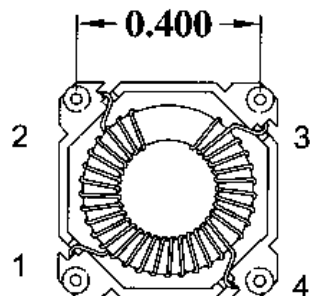
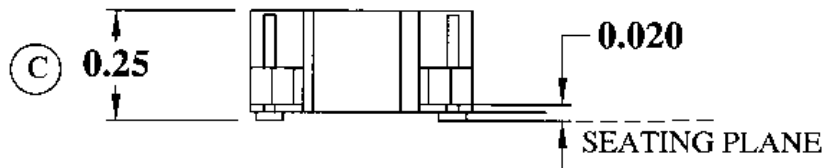
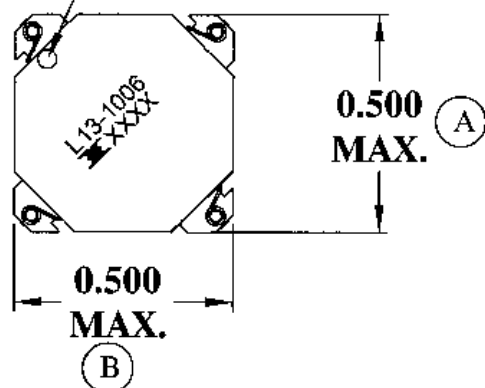


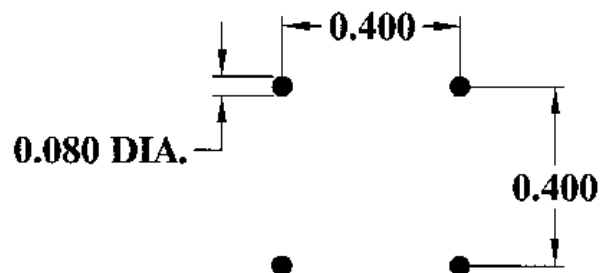
MECHANICAL

PIN 1 MARKING

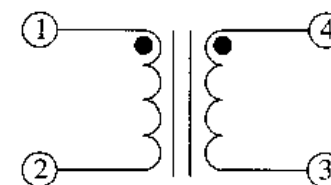


COPLANARITY 0.004

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 4.86μH±10% @ 0 ADC
(1, 4 - 2, 3) 10 KHz 50mV

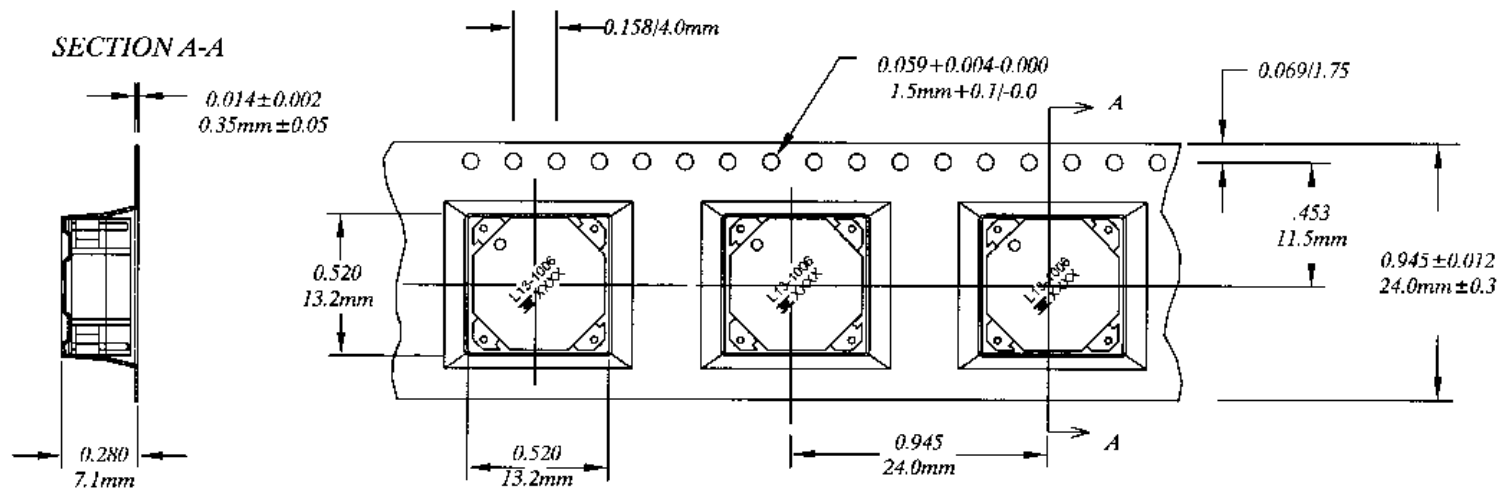
DC BIAS: 3.16uH TYP. @ 7.86 ADC (1, 4 - 2, 3) 10 KHz 50mV

DC RESISTANCE: 0.0198 OHMS MAX. (1, 4 - 2, 3)

IR: 100 VDC (1-4) 1K MEGOHMS MIN.

RoHS Compliant

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				☐ CUSTOM ☒ NON-PROPRIETARY	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	TITLE:
B	3-11-08	CHANGE ELECTRICAL		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	QTR PAK™ (Switching Regulator)
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			DRAWN:	K. LOFTUS			
				ENGR:	J. DECRAMER	TITLE:	QTR PAK™ (Switching Regulator)
B	3-11-08	CHANGE ELECTRICAL		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO.	
A	1-19-06	CUSTOMER PRINT				L13-1006	
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2			