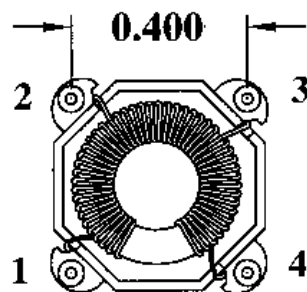
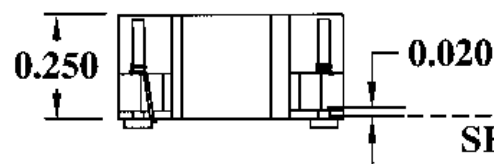
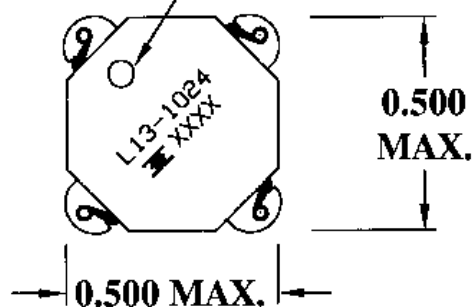


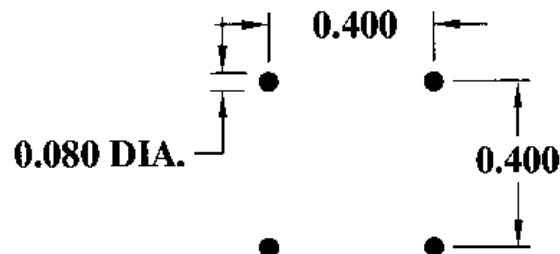
MECHANICAL

PIN 1 MARKING

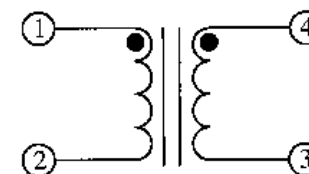


COPLANARITY TM0070

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: $43.78\mu\text{H} \pm 10\%$ (1, 4 - 2, 3)
10 KHz 100mV

DC BIAS: $27.14\mu\text{H}$ TYP. @ 2.81 ADC (1, 4 - 2, 3) 10 KHz 100mV

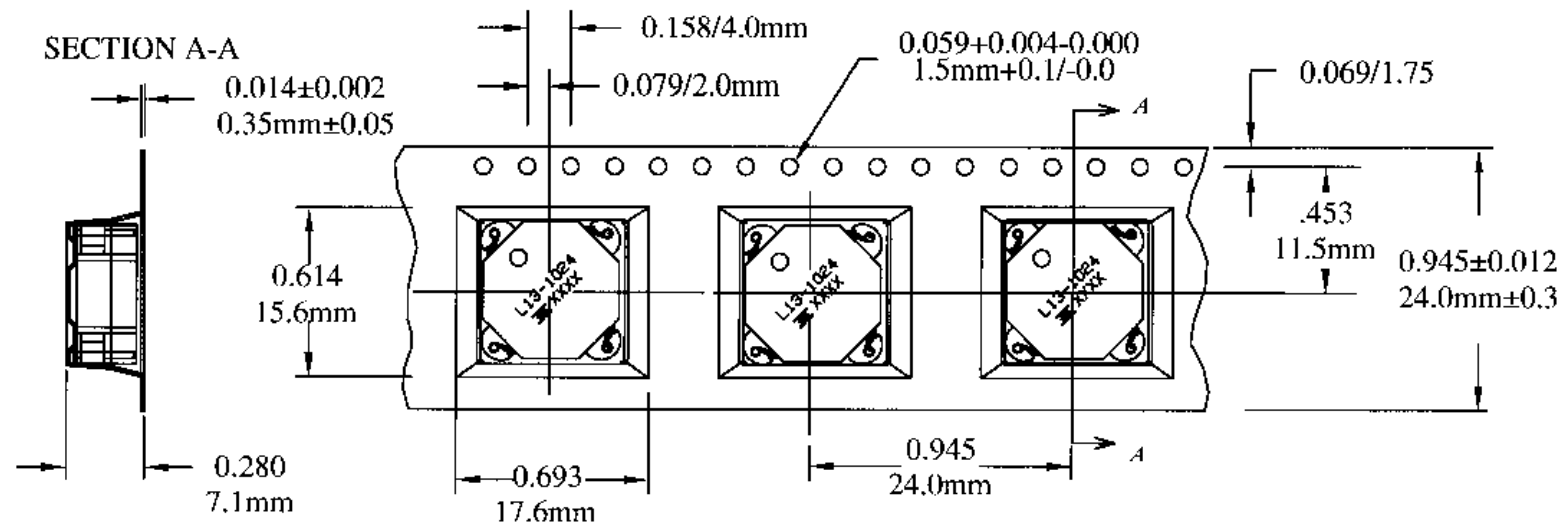
DC RESISTANCE: 0.378 OHMS MAX. (1-2) (4-3)

IR: 100 VDC (1-4) 1K MEGOHMS MIN.

PRIMARY LEAKAGE INDUCTANCE: $0.315\mu\text{H}$ NOM. (1-2 SH 4, 3)
120 KHz 50mV

RoHS Compliant

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			☐ CUSTOM ☒ NON-PROPRIETARY		
			DRAWN: K. LOFTUS		
C	2-5-13	UPDATE	JDC	ENGR: J. DECRAMER	TITLE: QTR PAK™ (Switching Regulator)
B	10-29-07	REVISE SEATING PLANE	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	
A	5-8-07	CUSTOMER PRINT	JDC		PAGE 1 OF 2
ISSUE	DATE	REVISION	BY	PART NO. L13-1024	



CARRIER TAPE 811-0137

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				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	
C	2-5-13	UPDATE		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES. TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	TITLE: QTR PAK™ (Switching Regulator)
B	10-29-07	REVISE SEATING PLANE	JDC		
A	5-8-07	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2	PART NO. L13-1024