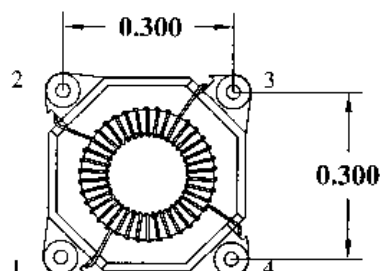
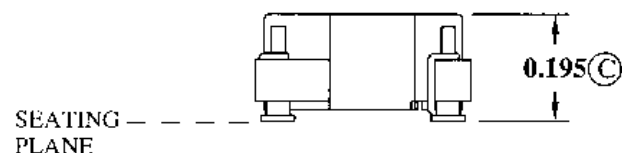
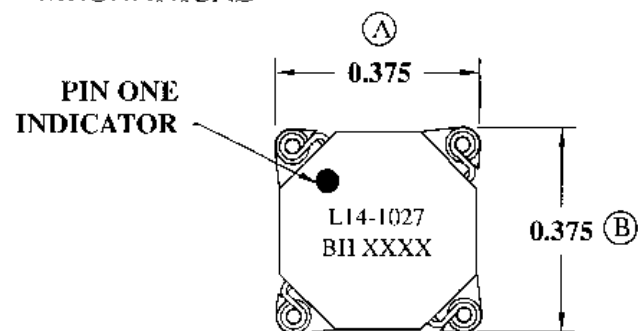
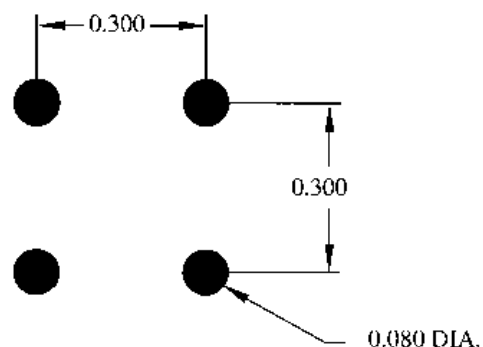


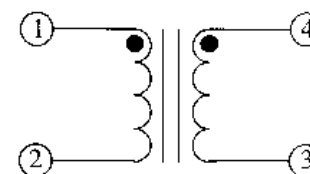
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

Turns Ratio: 1:1

Open Circuit Inductance: 11.49 μ H $\pm$ 10% @ 0 ADC (1, 4 - 2, 3)

10 KHz 50mV

DC Bias: 7.47 μ H TYP. @ 2.93 ADC (1, 4 - 2, 3)

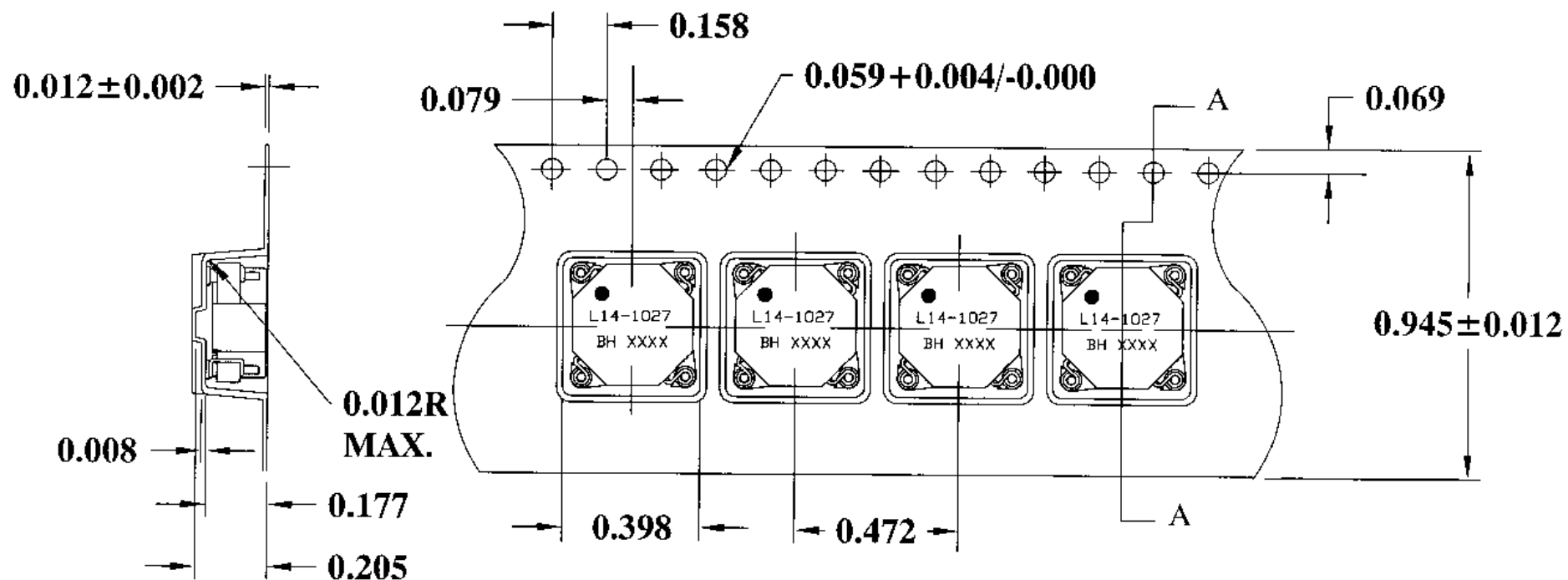
10 KHz 50mV

DC Resistance: 0.082 OHMS MAX. (1, 4 - 2, 3)

IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

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				ENGR:	J. DECRAMER	TITLE:
C	10-29-07	REVISE ELECTRICALS	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 1 OF 2		INDUCTOR
B	2-9-06	REVISE ELECTRICALS	JDC			
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B	2-9-06	REVISE ELECTRICALS	JDC				
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ISSUE	DATE	REVISION	BY	PAGE 2 OF 2			