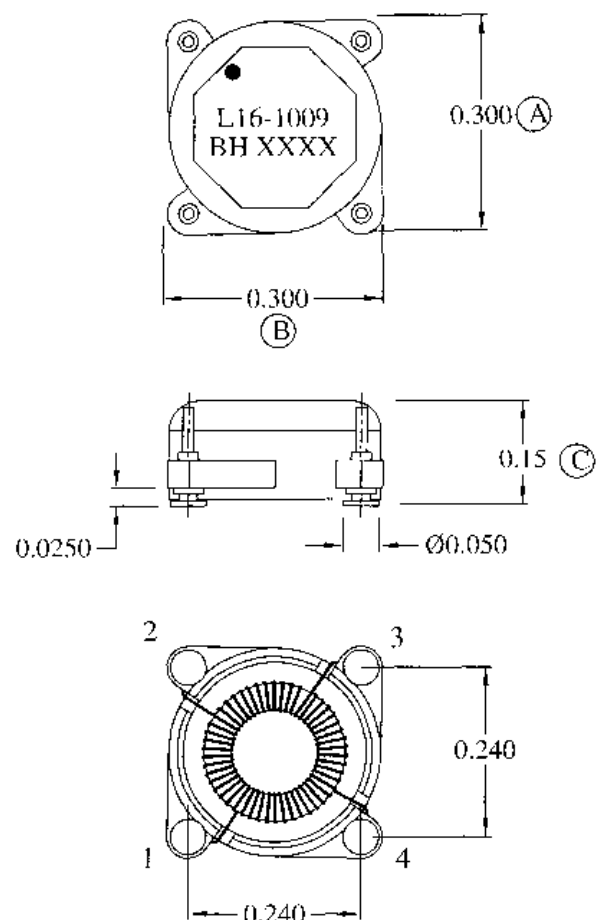
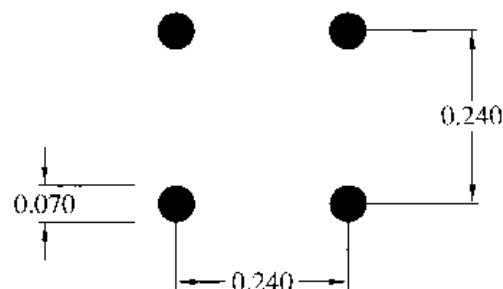


MECHANICAL

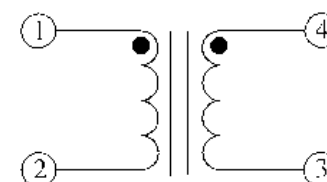


COPLANARITY 0.004 MAX.

SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: $4.19\mu\text{H} \pm 20\%$ @ 0 ADC (1, 4 - 2, 3)
10 KHz 100mV

DC BIAS: $3.35\mu\text{H}$ TYP. @ 3.78 ADC (1, 4 - 2, 3)
10 KHz 100mV

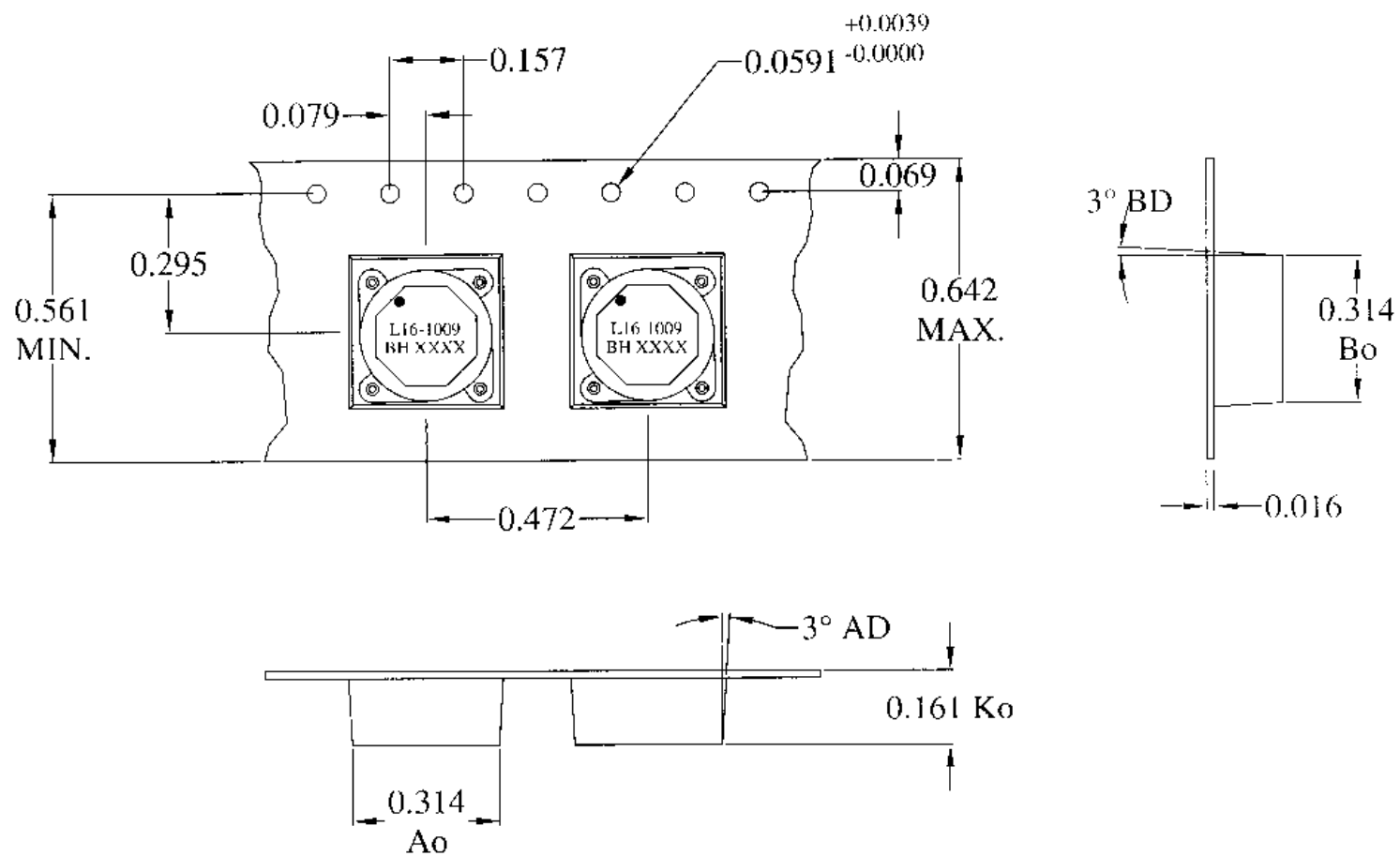
DC RESISTANCE: 0.041 OHMS MAX. (1, 4 - 2, 3)

IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

PRIMARY LEAKAGE INDUCTANCE: 90nH NOM. (1-2 SH 3, 4)
120 KHz 50mV

RoHS COMPLIANT

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				☐ CUSTOM	☒ NON-PROPRIETARY	
				DRAWN:	K. LOFTUS	
				ENGR:	J. DECRAMER	TITLE:
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO.
				PAGE 1 OF 2		
C	11-30-11	REVISE DIMENSIONS		INDUCTOR		
B	10-17-08	ADD PAGE 2	JDC	L16-1009		
A	12-10-10	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY			



				DESIGN PER:	BH CATALOG ☐ CUSTOM NON-PROPRIETARY ☒ DRAWN: K. LOFTUS ENGR: J. DECRAMER UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 2 OF 2	 [®] BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN [©] 1998 BH Electronics
C	11-30-11	REVISE DIMENSIONS				TITLE:
B	10-17-08	ADD PAGE 2	JDC			INDUCTOR
A	10-19-07	CUSTOMER PRINT	JDC			PART NO.
						L16-1009
ISSUE	DATE	REVISION	BY			