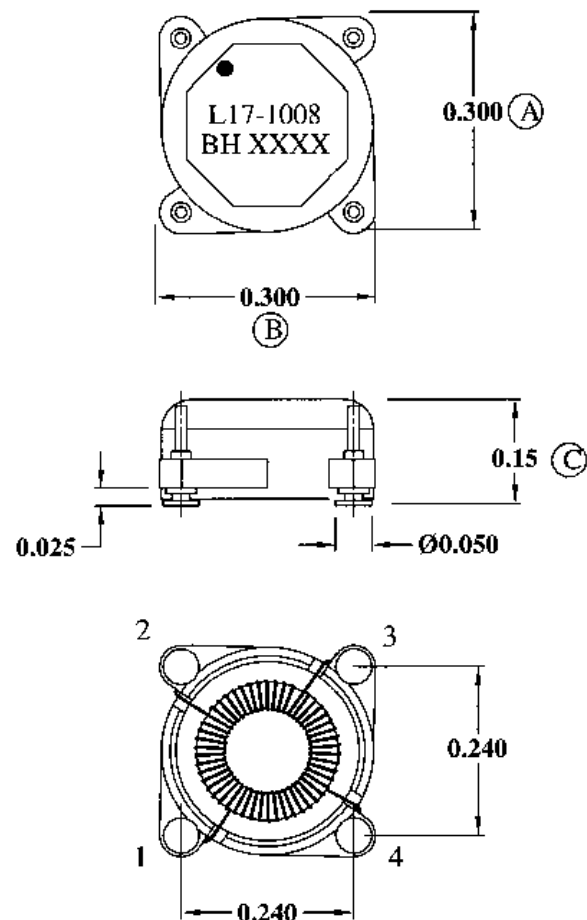
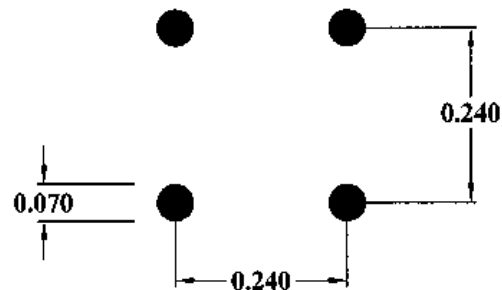


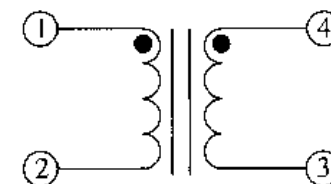
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: $3.33\mu\text{H} \pm 10\%$ @ 0 ADC (1, 4 - 2, 3)
10 KHz 10mV

DC BIAS: $2.16\mu\text{H}$ TYP. @ 3.87 ADC (1, 4 - 2, 3)
10 KHz 10mV

DC RESISTANCE: 0.038 OHMS MAX. (1, 4 - 2, 3)

IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

				DESIGN PER:	BH CATALOG	 [®] BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				☐ CUSTOM	NON-PROPRIETARY ☒	
				DRAWN:	K. LOFTUS	
				ENGR:	J. DECRAMER	TITLE:
B	9-17-09	REVISE DIMENSIONS	<i>JDC</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		INDUCTOR
A	10-22-07	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		PART NO.
						L17-1008