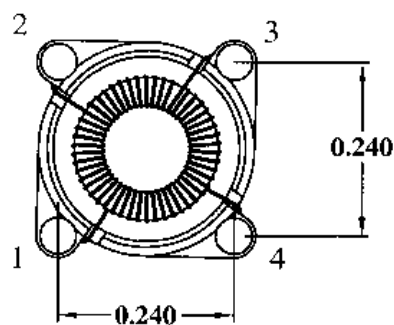
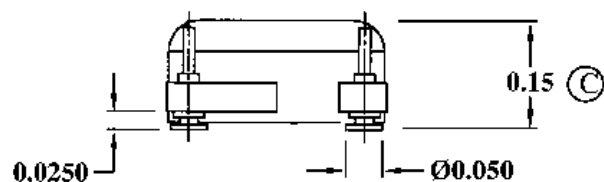
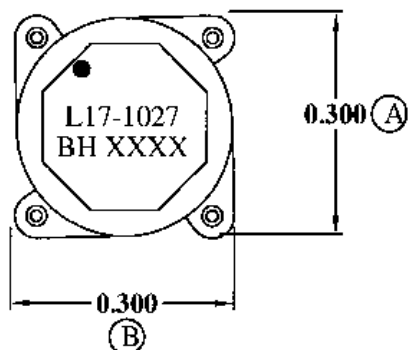
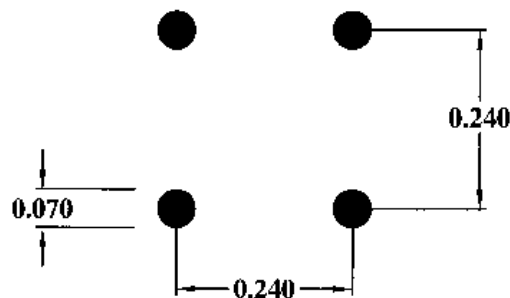


MECHANICAL

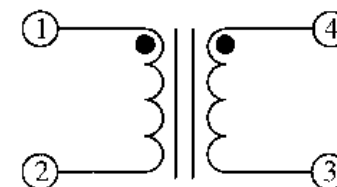


COPLANARITY 0.004 MAX.

SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 35.15 μ H $\pm$ 10% @ 0 ADC (1, 4 - 2, 3)
10 KHz 40mV

DC BIAS: 22.85 μ H TYP. @ 1.19 ADC (1, 4 - 2, 3)
10 KHz 40mV

DC RESISTANCE: 0.393 OHMS MAX. (1, 4 - 2, 3)

IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				☐ CUSTOM		
				DRAWN: K. LOFTUS		
				ENGR: J. DECRAMER		
C	10-21-13	PAD LAYOUT	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:
B	2-23-11	REVISE DCR SPEC	JDC			INDUCTOR
A	10-22-07	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		PART NO.
						L17-1027